## **DL405 CPU Specifications**

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	<u>D4-454</u>	
System Capacity		
Total memory available (words)	46.8K	
Ladder memory (words)		
built-in memory	31.5K	
with memory cartridge	N/A	
V-memory (words)	15.3K	
Battery backup	Yes, MRAM*	
Total CPU memory I/O pts. available		
(actual		
Ì/O points depend on I/O configuration selected)	2/4/8/16/32/64	
I/O module point density	4/6/8	
I/O module slots per base	1024 in/1024 out	
Local/local expansion	4224 max.	
Serial Remote I/O (including local &	3	
exp. I/O	512, 2048	
Remote I/O Channels	Yes,	
I/O pts. per remote module	8192 max.	
channel	(Including local and exp.	
Ethernet Remote I/O (including	1/0)	
local/exp. I/O)	Map into V-memory	
discrete I/O pts.	Limited by power budget	
	16,384 (16 fully expanded	
Analog I/O channels	H4-EBC Servers using	
Remote I/O channels	V-memory and bit-of-word	
I/O per remote channel	instructions)	
·	instructions)	
Destaura		
Performance	0.00	
Contact execution (boolean)	0.96 µs 4–5 ms	
Typical scan (1K boolean)	4-0 1115	
Programming and Diagnostics		
RLL ladder style	Yes	
RLL PLUS/flowchart style (Stages)	Yes/1024	
Run time editing	Yes	
Supports Overrides	Yes	
Variable/fixed scan	Fixed or variable	
Instructions	210	
Control relays	2048	
Timers	256	
Counters	256	
Immediate I/O	Yes	
Subroutines	Yes	
For/next loops	Yes	
Timed interrupt	Yes	
Integer math	Yes	
Floating-point math	Yes	
	Yes	
Trigonometric functions		
Trigonometric functions	Yes	
Table instructions	Yes	
Table instructions PID	Yes	
Table instructions PID Drum sequencers	Yes Yes	
Table instructions PID Drum sequencers Bit of word	Yes Yes Yes	
Table instructions PID Drum sequencers Bit of word Real-time clock/calendar	Yes Yes Yes Yes	
Table instructions PID Drum sequencers Bit of word Real-time clock/calendar Internal diagnostics	Yes Yes Yes Yes Yes	
Table instructions PID Drum sequencers Bit of word Real-time clock/calendar Internal diagnostics Password security	Yes Yes Yes Yes Multi-level	
Table instructions PID Drum sequencers Bit of word Real-time clock/calendar Internal diagnostics Password security System and User error log	Yes Yes Yes Yes Multi-level Yes	
Table instructions PID Drum sequencers Bit of word Real-time clock/calendar Internal diagnostics Password security System and User error log IBox instructions	Yes Yes Yes Yes Multi-level	
Table instructions PID Drum sequencers Bit of word Real-time clock/calendar Internal diagnostics Password security System and User error log IBox instructions CPU Ports Communications	Yes Yes Yes Yes Multi-level Yes Yes	
Table instructions PID Drum sequencers Bit of word Real-time clock/calendar Internal diagnostics Password security System and User error log IBox instructions CPU Ports Communications Built-in ports	Yes Yes Yes Yes Multi-level Yes Yes 4 ports	
Table instructions PID Drum sequencers Bit of word Real-time clock/calendar Internal diagnostics Password security System and User error log IBox instructions CPU Ports Communications Built-in ports K-sequence (proprietary protocol)	Yes Yes Yes Yes Multi-level Yes Yes 4 ports Yes	
Table instructions PID Drum sequencers Bit of word Real-time clock/calendar Internal diagnostics Password security System and User error log IBox instructions <b>CPU Ports Communications</b> Built-in ports K-sequence (proprietary protocol) DirectNET	Yes Yes Yes Yes Multi-level Yes Yes Yes 4 ports Yes Yes	
Table instructions PID Drum sequencers Bit of word Real-time clock/calendar Internal diagnostics Password security System and User error log IBox instructions <b>CPU Ports Communications</b> Built-in ports K-sequence (proprietary protocol) DirectNET Modbus Client/Server	Yes Yes Yes Yes Multi-level Yes Yes Yes Yes Yes Yes Yes	
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