

# Counter Interface Module

## COUNTER INTERFACE MODULE

**D2-CTRINT \$150.00**



*Note: D2-262 does not support the D2-CTRINT*

## Overview

The D2-CTRINT offers several modes of operation that can be used to solve simple motion and high-speed machine control applications.

The operating modes and module configuration are explained in detail in the D2-CTRIF-M High Speed Counter Manual. The high-speed input features cannot be used if the pulse output features are in use, and vice versa. Only one D2-CTRINT can be used per CPU and the module must reside in slot 0, next to the CPU. A brief description of each high-speed mode is listed below:

**Mode 10:** Up to two 5kHz high-speed counters offer 24 presets each. When the preset is reached, a CPU interrupt routine is executed.

**Mode 20:** Quadrature encoder input (up/down) for clockwise and counterclockwise position control (max. pulse range: -8,388,608 to 8,388,607).

**Mode 30:** Pulse outputs are programmable to follow a predetermined profile. An external interrupt can be used in conjunction with separate acceleration and deceleration profiles for positioning and velocity control supported by D2-250-1 (max. pulse range: -8,388,608 to 8,388,607 at 5K pulses per second max).

**Mode 40:** Four external interrupt inputs can be used for an immediate response for high-priority events.

**Mode 50:** Pulse catch feature allows the CPU to read 4 inputs, each having a pulse width as small as 0.1 ms. When an input pulse is detected, the input is set ON for the next scan and then resets.

Counter Interface Module Features					
Mode	Module Points				
	Input 0	Input 1	Input 2	Input 3	Input 4
<b>Mode 10: Two High Speed Up Counters</b>	Up counter 1	Up counter 2 Filtered input	Reset counter 1	Reset counter 2	Not used
<b>Mode 20: One Up/Down or Quadrature cntr</b>	Phase A input (up count)	Phase B input (down count)	Counter reset	Filtered input	Not used
<b>Mode 30: Pulse Output</b>	Filtered input	Filtered input	Not used	CW pulse output (or Pulse output)	CCW pulse out (or Direction)
<b>Mode 40: External Interrupts</b>	Interrupt input (not available when using timed interrupt)	Interrupt input	Interrupt input	Interrupt input	Not used
<b>Mode 50: Pulse Catch Inputs</b>	Pulse input	Pulse input	Pulse input	Pulse input	Not used
<b>Mode 60: Filtered Input</b>	Filtered input	Filtered input	Filtered input	Filtered input	Not used

*The high-speed input features cannot be used if the pulse output features are in use, and vice versa.*

Input Specifications	
<b>Input</b>	4 pts. sink/source 5 kHz max.
<b>Minimum pulse width</b>	100µs
<b>Input Voltage Range</b>	12 or 24 VDC ±15%
<b>Maximum voltage</b>	30VDC
<b>Rated input current</b>	10mA Typical 13mA Maximum
<b>Minimum ON voltage</b>	8.0 VDC
<b>Maximum OFF voltage</b>	1.0 VDC
<b>Minimum ON Current</b>	8.0 mA
<b>Maximum OFF Current</b>	1.0 mA
<b>OFF to ON Response</b>	Less than 30µs
<b>ON to OFF Response</b>	Less than 30µs

**Mode 60:** Input filters are configurable (0-99ms) to ensure input signal integrity. The default input mode is a 10ms filter.

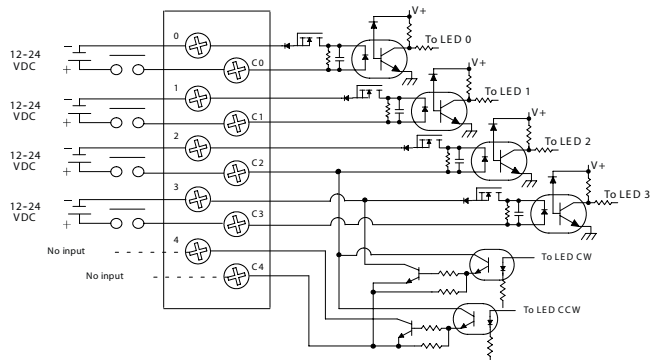
**Mode 40 option** - A Timed interrupt can be configured for time critical events. Interrupt 0 can be scheduled on a 3ms-999ms cycle. See the next page for more information on the timed interrupt.

Output Specifications	
<b>Output</b>	2 pts., current sinking 5kHz Max
<b>Voltage range</b>	5.0 VDC±15%
<b>Maximum voltage</b>	5.5 VDC
<b>Maximum load current</b>	30mA
<b>Minimum load voltage</b>	4.5 VDC
<b>Leakage current</b>	Less than 0.1 mA at 5.5 VDC
<b>Inrush current</b>	0.5 A (10ms)
<b>OFF to ON Response</b>	Less than 30µs
<b>On to OFF Response</b>	Less than 30µs
<b>External power supply</b>	5.0 VDC ±10%

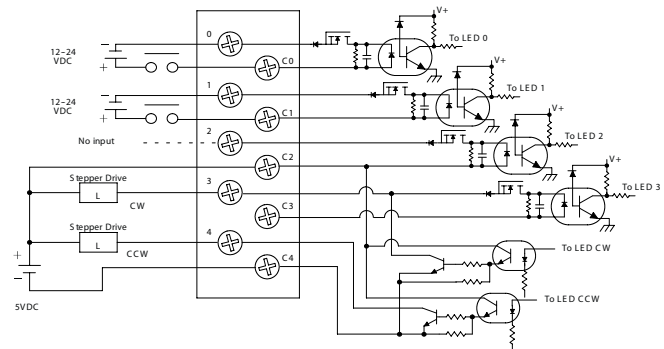
General Specifications	
<b>Module Type</b>	Discrete
<b>Modules per CPU</b>	One only in slot adjacent to CPU
<b>I/O Points Used</b>	8 inputs, 8 outputs
<b>Field Wiring Connector</b>	Standard 8 pt. removable terminal block
<b>Internal Power Consumption</b>	50mA from 5VDC max., (supplied by the CPU base power supply)
<b>Operating Environment</b>	32°F to 140°F (0°C to 60°C) humidity (non-condensing) 5% to 95%
<b>Manufacturer</b>	Koyo Electronics

# Counter Interface Module

## Wiring Diagram for Modes 10, 20, 40, 50 and 60



## Wiring Diagram for Mode 30



## Timed Interrupt feature

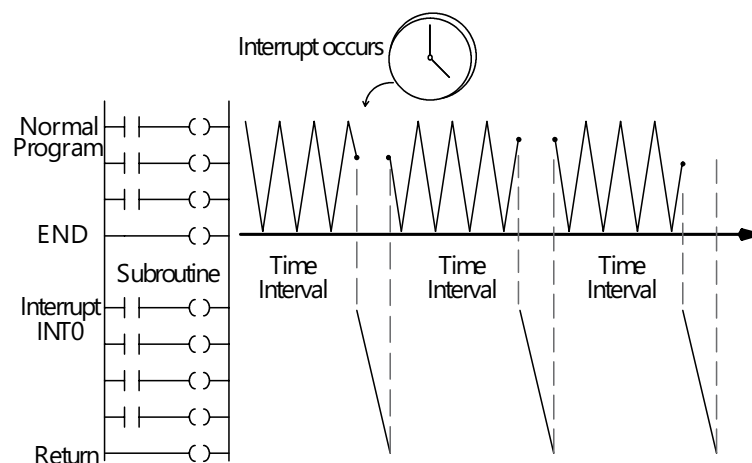
There is an internal timed interrupt feature available in the D2-262 CPU. You do not need the Counter Interface module to use the timed interrupt. This cyclical interrupt allows you to easily program a time-based interrupt that occurs on a scheduled basis. The CPU's timed interrupt operates in a similar manner to the external interrupt input, but instead of the interrupt subroutine being triggered by an external event, it is now triggered by a cyclical interval of time. This interval can be programmed from 3ms to 999ms. Whenever the programmed time elapses, the CPU immediately suspends its routine scan cycle and jumps to interrupt subroutine INT 0. As with the other modes, when the subroutine execution is complete, the CPU automatically resumes its routine scan cycle starting at the exact location where it was interrupted. Since the CPU scan time and the interrupt time interval are different, the program gets interrupted at various points in the execution over time. The CPU returns to the point where it left to resume the program execution.

If you use a timed interrupt and the Counter Interface module, Input 0 cannot be used on the Counter Interface module. If you're using the timed interrupt and a standard discrete input module, then there are no limitations.

## Timed Interrupt Specifications

<b>Timed interrupts</b>	One (internal to CPU)
<b>Time interval</b>	3ms to 999ms (1ms increments)
<b>Interrupt Subroutine</b>	INT0

## Timed interrupt operation





# Dimensions and Installation

Understanding the installation requirements for your DL205 system will help ensure that the DL205 products operate within their environmental and electrical limits.

## Plan for safety

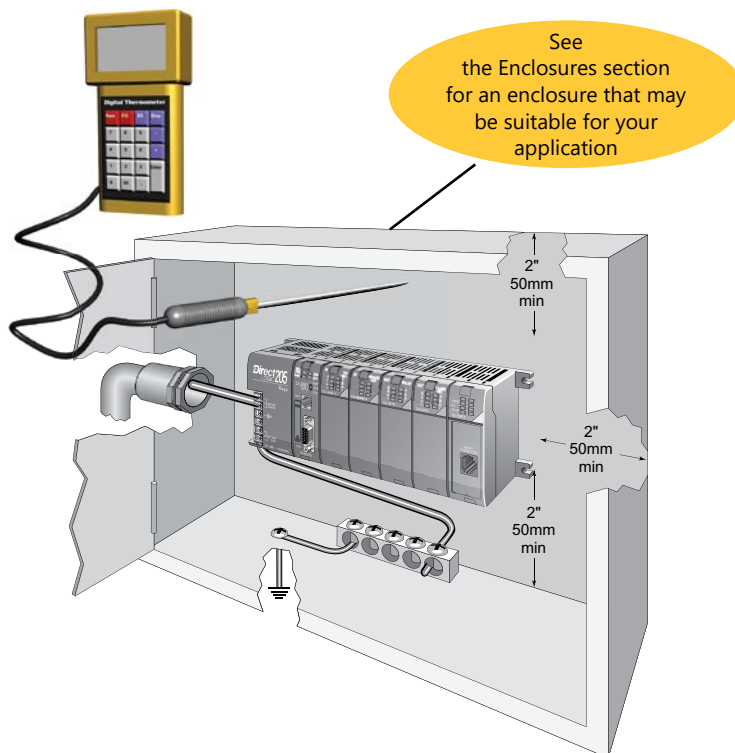
This catalog should never be used as a replacement for the user manual. The user manual, D2-USER-M (downloadable online), contains important safety information that must be followed. The system installation should comply with all appropriate electrical codes and standards.

## Environmental specifications

The Environmental Specifications table at the right lists specifications that apply globally to the DL205 system (CPUs, bases, and I/O modules). Be sure that the DL205 system is operated within these environmental specifications.

## Base dimensions and mounting

Use the diagrams below to make sure the DL205 system can be installed in your application. To ensure proper airflow for cooling purposes, DL205 bases must be mounted horizontally. It is important to check these dimensions against the conditions required for your application. For example, it is recommended that approximately 3" of space is left in front PLC surface for ease of access and cable clearances. Also, check the installation guidelines for recommended cabinet clearances.



Environmental Specification	Rating
<b>Storage Temperature</b>	-4°F to 158°F (-20°C to 70°C)
<b>Ambient Operating Temperature</b>	32°F to 131°F (0°C to 55°C)
<b>Ambient Humidity</b>	30% to 95% relative humidity (non-condensing)
<b>Vibration Resistance</b>	MIL STD 810C, Method 514.2
<b>Shock Resistance</b>	MIL STD 810C, Method 516.2
<b>Noise Immunity</b>	NEMA (IC33-304)
<b>Atmosphere</b>	No corrosive gases

Base	A	B	C	D
<b>D2-03B-1, D2-03BDC1-1</b>	6.77" 172mm	6.41" 163mm	5.8" 148mm	7.24" 184mm
<b>D2-04B-1, D2-04BDC1-1</b>	7.99" 203mm	7.63" 194mm	7.04" 179mm	8.46" 215mm
<b>D2-06B-1, D2-06BDC1-1, D2-06BDC2-1</b>	10.43" 265mm	10.07" 256mm	9.48" 241mm	10.90" 277mm
<b>D2-09B-1, D2-09BDC1-1, D2-09BDC2-1</b>	14.09" 358mm	13.74" 349mm	13.14" 334mm	14.56" 370mm

