

## Protocol description 2-wire-interface for electronic circuit breakers 787-166x-series

		Signal stream for the electronic circuit breakers								
Description	Byte 1	Byte 2	Byte 3	Byte 4	Byte 5	Byte 6	Byte 7		Byte 8	Byte 9
		9 10 11 12 13 14 15 16 9 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	17 18 19 20 21 22 23 24 	25 26 27 28 29 30 31 32	32 33 34 35 36 37 38 39 	40 41 42 43 44 45	46 47 48 49 50 51 52 53	54 55 56 57 58 59	60 61 62 63 64 65 6	16 67 68 69 70 71 72 73 74 75
Task in the PLC						uuuuu	urran			
Data stream from PLC to Device: The lower Manchester encoding is decrypted in the circuit breaker as follows.	0 0 0 1 0 1 0 1	1 1 0 0 0 1 1 0	0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0	0 0 0 0 0 0 0 0	0 0 0 0 0	0 0 0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0 0	0 0 0 0 0 0 0 0 0 0
PLC Output (S1 Input Circuit Breaker) Auxiliary Clock XOR Data Bits:	ymcBit Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0	0 Bit 7 Bit 6 Bit 5 Bit 4 Bit 3 Bit 2 Bit 1 Bit 0	Bit7 Bt6 Bit5 Bit4 Bit3 Bit2 Bit1 Bit0	Bit 7 Bit 6 Bit 5 Bit 4 Bit 3 Bit 2 Bit 1 Bit	itO Bit7 Bit6 Bit5 Bit4 Bit3 Bit2 Bit1	Bt0 Bt7 Bt6 Bt5 Bt4 Bt3	Bt2 Bt1 Bt0 Bt7 Bt6 Bt5 Bt4 Bt3	Bt2 Bt1 Bt0 Bt7 Bt6 Bt5	Bt-4 Bt 3 Bt 2 Bt 1 Bt 0 Bt 7 Bt 1	J BE5 BE4 BE3 BE2 BE1 BE0 BE7 BE6 BE5
A digital output of the controller sends the Manchester coding to the circuit breaker vis "31". This coding sportles which output channel should be withche on or off. The 's tipped also has a sum meet forcing. ATV signal must only be applied fort> 0.5 seconds to trigger a reset.	Manye3 Chamel's Chame	And Date of the second date of t	Long Protocol: Auxiliary Clock Short Protocol: Checksum	Auxiliary Clock	Auxiliary Clock					
PLC Input (S2 Output Circuit Breaker) Data Bits, Valid on the Falling Clock Edge:	0 0 0 1 0 0 0 1 0 0 0 0	0 Bt7 Bt6 Bt5 Bt4 Bt3 Bt2 Bt1 Bt0 . 1 1 0 0 0 1 1 0	BE7 BE6 BE5 BE4 BE3 BE2 BE1 BE0   1 1 0 0 0 0 0 0 0	Bit 7 Bit 6 Bit 5 Bit 4 Bit 3 Bit 2 Bit 1 Bit 3   0 0 0 0 1 0 0 0	Bit 0 Bit 7 Bit 6 Bit 5 Bit 4 Bit 3 Bit 2 Bit 1   0 0 0 1 0 0 0 0	0 0 0 1 0 0	Bit 2 Bit 1 Bit 0 Bit 7 Bit 6 Bit 5 Bit 4 Bit 3   0 0 0 0 0 0 1 0	Bit 2 Bit 1 Bit 0 Bit 7 Bit 6 Bit 5   0 0 0 0 0 0 0	Bit 4 Bit 3 Bit 2 Bit 1 Bit 0 Bit 7 Bit 1   1 0 0 0 0 0 0 0	6 84.5 84.4 86.3 86.2 86.1 86.0 86.7 86.6 86.5   0 0 1 0
The density breaker internality synchronisms to this and at the same time sends back the status (on/off and error status) of all channels var "32". Optionally, in addition to the module lequir voltage, the actual flowing current and the set current value of each circuit can be quented, see "Extended protocol". The data extensed by the circuit breaker is high/low only and on Manchester coded. In	ahayso Chareed	el 1 Charonel 8 Charonel 2 Charonel 6 Charonel 4 Charonel 1 Charon	Long Protocol: Current Input Voltage Short Protocol: Checksum	The set nominal current or actual flowing current channel 1	The set nominal current or actual flowing current channel 2				· · · · · · · · · · · · · · · · · · ·	· · · · · · · · · · · · · · · · · · ·
order to avoid their againing due to groups more times are delayed of the U(2) is a GPC, It is spould to experi are develowed as the hole in the protocolar that papers, but cloud broader must be softmered at the configuration by the data data stress must be autoprotect delayed more stress. See that a data stress must be autoprotect delayed more stress de- data and gravity with a delayed more delayed more stress de- data and gravity and the delayed more the transmission of data and gravity bits delayed more than the transmission of the data and gravity and a delayed more than the transmission delay and gravity bits delayed more than the transmission and and a streng brain is according to the transmission of the transmission and equal to the transmission of the data strength with the brander is undergrine strength bits, the RC cannot send another bits.	Short Pro without Ch	necksum Short Protocol A stop bit		pool with Checksum comes after the 24th bit fo cycles.		Long Protocol <u>without</u> Chec	:ksum			
		Long Protocol with Checksum								
Data from PLC to Device (51)	Byte 1: Switch Channels On/Off With the first byte the channels can be switched on (HiGH) or switched off (LOW). For this, the the stote-setting bit (Bit 7) must be set in the configuration byte.	Byte 2: Configuration byte   Bit 7: Con/Off - Set state (verso)   Bit 6: Set state (verso)	Byte 3: Auxiliary Clock The fuse requires an auxiliary clock via the 51 inputs a long as the fuse transmits data via the 52 output to the PLC.	Bytes 4 - 11: Auxiliary Clock The fuse requires an auxiliary clock via the as long as the fuse transmits data via the S	S2 output to the PLC.					
Data from the Device to the PLC (S2)	Byte 1: On/Off Operating States HIGH: Switched Channel LOW: Disconnected Channel	Byte 2: Channel States Overcurrent/Tripped HIGH: Channel n is in the overcurrent or tripped state. LOW: Channel n is not in the overcurrent or tripped state.	$\begin{array}{l} \textbf{Pyte 3: Current Input Voltage}\\ The actual input voltage is in \\ an inst_{1} data type \\ (Range: to 255).\\ This transferred value must \\ be converted to a physical value \\ in the PLC.\\ 12V-Variant:\\ U_{in} = \left(\frac{transferred value}{16} + 8\right) \\ 24V-Variant:\\ U_{im} = \left(\frac{transferred value}{16} + 16\right) \\ 48V-Variant:\\ U_{im} = \left(\frac{transferred value}{8} + 32\right) \end{array}$	The set nominal current is only output if q	at Channel n if the query W in the sent in an Int8_t data type	HIGH in the configuration byte (Byt				

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