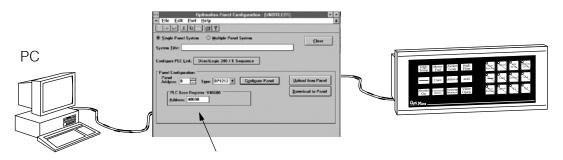
Applying Ladder Logic

General Concepts

Memory Mapping

The OP-1212 uses memory mapping in order to link itself to a PLC. Memory mapping is a technique that maps the memory of the OP-1212 to the memory of the PLC. During initial configuration, the beginning address must be selected in the PLC memory where the mapping process will start. By knowing where the data of the specific panel is mapped, this data can be moved, changed or monitored using ladder logic.



During configuration, you determine the starting address for the memory mapping process.

DirectLOGIC

Mapping Assignments

1	2	3	4	1234
5	6	7	8	5678
9	10	11	12	9 10 11 12
Opti Mate				

Mapped Memory Location	Function
m+0 (such as V40600) C0-C17	Indicator Lamps ON/OFF
m+1 (such as V40601) C20-C37	Indicator Lamps Flash Control
m+2 (such as V40602) C40-C57	Button LEDs ON/OFF
 m+3 (such as V40603) C60-C77	Button LEDs Flash Control
m+4 (such as V40604) C100-C117	Button ON/OFF Status
m+5 (such as V40605) C120-C137	Force Pushbuttons Data & Comnd

The pushbuttons and lamps are numbered left to right starting in the upper left corner of their respective area.

Allen-Bradley



Mapped Memory Location	Function
m+0 (such as N7: 0/0-0/15)	Indicator Lamps ON/OFF
m+1 (such as N7: 1/0-1/15)	Indicator Lamps Flash Control
m+2 (such as N7: 2/0-2/15)	Button LEDs ON/OFF
m+3 (such as N7: 3/0-3/15)	Button LEDs Flash Control
m+4 (such as N7: 4/0-4/15)	Button ON/OFF Status
m+5 (such as N7: 5/0-5/15)	Force Pushbuttons Data & Comnd

Addressing Conventions

Before going into ladder logic programming, it is good to take a moment to review and compare the addressing conventions used by Automation**Direct** and Allen-Bradley.

DirectLOGIC Memory - A typical address within a **Direct**LOGIC PLC is Vxxxx, such as V40600 for **Direct**LOGIC PLCs (DL05, DL06, DL105, DL205, DL350 and DL405 families) and Rxx, such as R16 for the DL305 family. The V-memory in the **Direct**LOGIC PLCs is divided into 16-bit registers, and the R-memory in the DL305 is divided into 8-bit registers. Refer to your individual User Manuals for complete memory information. The two diagrams below shows how the OP-1212 could be mapped during configuration. In this example, V40600 and R16 have been chosen as starting registers to map the OP-1212 to the PLC, but it could actually be any available user or internal relay memory areas as long as they are consecutive:

DL05, DL06, DL105, DL205 or DL405

1	5 14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	◄ bit
Γ				12	11	10	9	8	7	6	5	4	3	2	1	V40600 - Indicator Lamp ON/OFF
				12	11	10	9	8	7	6	5	4	3	2	1	V40601 - Indicator Lamp Flash
				12	11	10	9	8	7	6	5	4	3	2	1	V40602 - Button LEDs ON/OFF
				12	11	10	9	8	7	6	5	4	3	2	1	V40603 - Button LEDs Flash
				12	11	10	9	8	7	6	5	4	3	2	1	V40604

DL305

	7	6	5	4	3	2	1	0	
Γ					12	11	10	9	R17
						11	10	9	R21
Γ					12		10	9	R23
Γ					12	11	10	9	R25
Γ					12	11	10	9	R27
Ν	И1	M2	MЗ		12	11	10	9	R31

M1M2M3

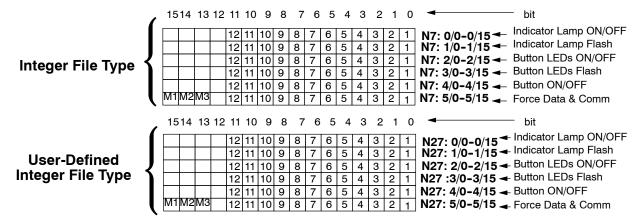
7	6	5	4	3	2	1	0	◄ bit
8	7	6	5	4	3	2	1	B16 Indicator Lamp ON/OFF
8	7	6	5	4	3	2	1	R20 - Indicator Lamp Flash
8	7	6	5	4	3	2	1	B22 Button LEDs ON/OFF
8	7	6	5	4	3	2	1	R24 - Button LEDs Flash
8	7	6	5	4	3	2	1	R26 - Button ON/OFF
8	7	6	5	4	3	2	1	R30 - Force Data & Comm

12 11 10 9 8 7 6 5 4 3 2 1 **V40605** Force Data & Comm

After the address has been selected and mapped, it will allow the ladder logic to treat pushbuttons as contacts and Lamps, and LEDs as coils. The following table is an example of the control relay correlation for *Direct*LOGIC PLCs to the OP-1212 when the address is configured for V40600. Use the work sheet in **Appendix A** for your application.

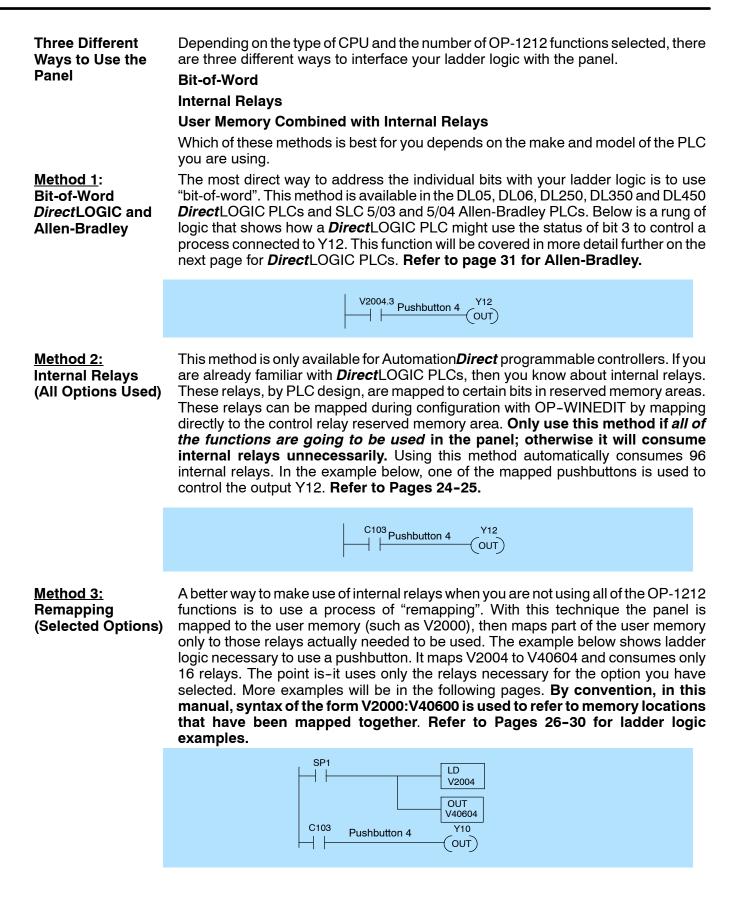
Device	Lamp ON/OFF	Lamp Flash	Button LED ON/ OFF	Button LED Flash	Button Status	Force Function
1	C0	C20	C40	C60	C100	C120
2	C1	C21	C41	C61	C101	C121
3	C2	C22	C42	C62	C102	C122
4	C3	C23	C43	C63	C103	C123
5	C4	C24	C44	C64	C104	C124
6	C5	C25	C45	C65	C105	C125
7	C6	C26	C46	C66	C106	C126
8	C7	C27	C47	C67	C107	C127
9	C10	C30	C50	C70	C110	C130
10	C11	C31	C51	C71	C111	C131
11	C12	C32	C52	C72	C112	C132
12	C13	C33	C53	C73	C113	C133
M3						C135
M2						C136
M1						C137

Allen-Bradley Memory-A typical address for Allen-Bradley might be N7:0/0 or N27:0/0. The OP-1212 will allow you to define your starting address for mapping purposes using either Allen-Bradley's integer (N7) file type or *user-defined* integer file types (N9-N255). *If you plan to use an integer file between N9 and N255, it must be defined in the Allen-Bradley memory map before configuring the panel.* Below diagrams show how 16-bit integer files could be used to map the pushbuttons to the Allen-Bradley PLC.



After the address has been selected and mapped, it will allow the ladder logic to treat pushbuttons as contacts and Lamps, and LEDs as coils. The following table is an example of the control relay correlation for the SLC or Micrologix to the OP-1212 when the address is configured for N7:0. Use the work sheet in **Appendix A** for your application.

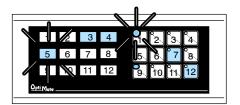
Device	Lamp ON/OFF	Lamp Flash	Button LED ON/ OFF	Button LED Flash	Button Status	Force Function
1	N7:0/0	N7:1/0	N72/0	N7:3/0	N7:4/0	N7:5/0
2	N7:0/1	N7:1/1	N7:2/1	N7:3/1	N7:4/1	N7:5/1
3	N7:0/2	N7:1/2	N7:2/2	N7:3/2	N7:4/2	N7:5/2
4	N7:0/3	N7:1/3	N7:2/3	N7:3/3	N7:4/3	N7:5/3
5	N7:0/4	N7:1/4	N7:2/4	N7:3/4	N7:4/4	N7:5/4
6	N7:0/5	N7:1/5	N7:2/5	N7:3/5	N7:4/5	N7:5/5
7	N7:0/6	N7:1/6	N7:2/6	N7:3/6	N7:4/6	N7:5/6
8	N7:0/7	N7:1/7	N7:2/7	N7:3/7	N7:4/7	N7:5/7
9	N7:0/8	N7:1/8	N7:2/8	N7:3/8	N7:4/8	N7:5/8
10	N7:0/9	N7:1/9	N7:2/9	N7:3/9	N7:4/9	N7:5/9
11	N7:0/10	N7:1/10	N7:2/10	N7:3/10	N7:4/10	N7:5/10
12	N7:0/11	N7:1/11	N7:2/11	N7:3/11	N7:4/11	N7:5/11
МЗ						N7:5/13
M2						N7:5/14
M1						N7:5/15



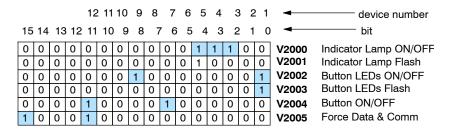
Using Bit-of-Word with the OP-1212

V2000.2

Using Ladder Logic **Diredt**LOGIC PLCs (DL05, DL06, DL250, DL350 and DL450) all use the bit-of-word instructions. (Refer to your particular PLC user guide). The example program shown below uses a base register address of **V2000** to map the status of the pushbuttons, lamps, and LEDs. The ladder logic example provides a simple use for all of the panel features. If you are unfamiliar with any of the panel features, please refer to **Understanding the OP-1212 Panel**. The table shows which bits the program sets.



V2004.6



OUT) 1 1 Pushbutton 7 Lamp 3 V2000.3 (out) Lamp 4 X12 V2002.8 Light LED 9 2 ┥┝ OUT) X13 V20004 Light Lamp5 3 -| |· OUT) X14 V2001.4 Add flashing ┥┝ 4 OUT) X15 V2002.0 Light LED 1 5 -| |-OUT) X14 V2003.0 Add flashing 6 ┥┝ OUT) Pushbutton 12 ON Start Process Y10 V2004.11 OUT) 7 - + Process Finished X16 V2005.11 -| |-8 (оит) V2005.15 -(OUT) Pushbutton 12 OFF

Rung 1 - Pushbuttons and Lamps

When pushbutton 7 is activated Lamps 3 and 4 turn ON.

Rung 2 - LEDs

When contact X12 is ON, LED 9 turns ON NOTE: Panel must be in LED Separation mode and pushbutton configured as momentary.

Rungs 3 and 4 - Flashing Lamps

To flash a Lamp, it must first be turned ON. When contact X13 is activated Lamp 5 will turn ON and when contact X14 is activated the Lamp will flash.

Rungs 5 and 6 - Flashing LEDs

To flash an LED, it must first be turned ON. When contact X15 is activated, LED 1 will turn ON and when contact X14 is activated the LED will flash. NOTE: Panel must be in LED Separation mode and pushbutton configured as momentary.

Rungs 7 and 8 - Force Function

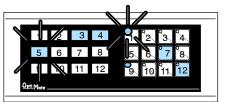
When pushbutton 12 is pressed, process Y10 is started. When the process is completed it activates contact X16 which forces pushbutton 12 OFF. NOTE: The pushbuttons must be configured as maintained (alternate) and the panels "Force Function" feature must be

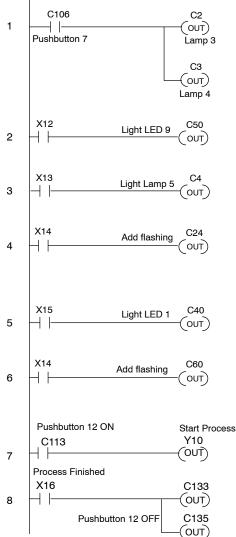
(alternate) and the panels "Force Fur enabled.

Using All Functions with DirectLOGIC PLCs

Using Ladder Logic

When configuring the OP-1212, a base address must be selected in the CPU. This address can be a *direct* mapping to the reserved memory locations that are tied to internal relays. The internal relays of *Direct*LOGIC PLCs (DL05, DL06, DL105, DL205, DL350 and DL405) start at **V40600**. Using this method, the total mapping consumes 96 internal relays, which 75 are assigned to operator functions. This method is only used when all of the OP-1212 functions are utilized. In the examples below, **V40600** has been chosen as the starting address for *Direct*LOGIC PLCs. *Notice that the internal control relays are numbered in octal and not decimal.*





				12	2 11	10	9	8	7	6	5	4	(3 2	21	-	device number
17	16	15	14	13	12	11	10	7	76	5 5	54	13	3	2 1	I C) -	Internal Relay
0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	0	V40600	Indicator Lamp ON/OFF
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0		Indicator Lamp Flash
0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	V40602	Button LEDs ON/OFF
0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	V40603	Button LEDs Flash
0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	V40604	Button ON/OFF
0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	V40605	Force Data & Comm

Rung 1 - Pushbuttons and Lamps

When pushbutton 7 is activated Lamps 3 and 4 turn ON.

Rung 2 - LEDs

When contact X12 is ON, LED 9 turns ON NOTE: Panel must be in LED Separation mode and pushbutton configured as momentary.

Rungs 3 and 4 - Flashing Lamps

To flash a Lamp, it must first be turned ON When contact X13 is activated Lamp 5 will turn ON and when contact X14 is activated the Lamp will flash.

Rungs 5 and 6 - Flashing LEDs

To flash an LED, it must first be turned ON. When contact X15 is activated, LED 1 will turn ON and when contact X14 is activated the LED will flash. NOTE: Panel must be in LED Separation mode and pushbutton configured as momentary.

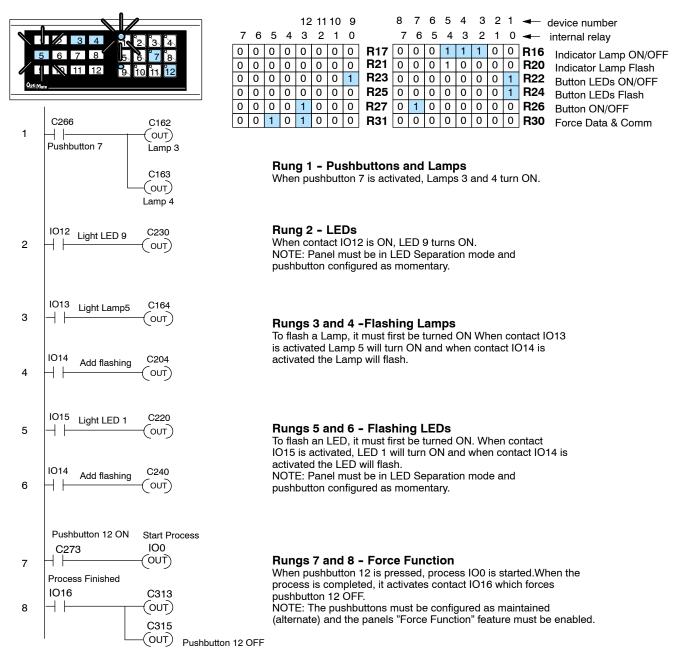
Rungs 7 and 8 - Force Function

When pushbutton 12 is pressed, process Y10 is started. When the process is completed, it activates contact X16 which forces pushbutton 12 OFF.

NOTE: The pushbuttons must be configured as maintained (alternate) and the panels "Force Function" feature must be enabled.

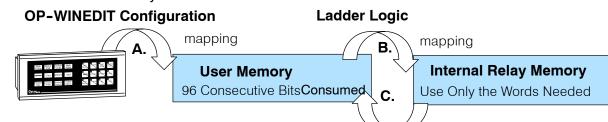
Using All Functions with the DL305 PLCs

Using Ladder Logic When configuring the OP-1212, a base address must be selected in the CPU. This address can be a *direct* mapping to the reserved memory locations that are tied to internal relays. The internal relays of the DL305 family start at **R16**. Using this method, the total mapping consumes 96 internal relays, of which 75 are assigned to operator functions. This method should only be used when all of the OP-1212 functions are utilized. In the examples below, **R16** has been chosen as the starting address for the DL305. *Notice that the internal control relays are numbered in octal and not decimal.*



Using Selected Functions with *Direct*LOGIC PLCs (not DL305 PLCs)

Using the
RemappingThe "remapping" process has been briefly discussed as a method that allows you to
easily manipulate individual bits to take advantage of the panels several functions.ProcessAll the functions are bit-controlled. By using this method, the number of relays
actually needed for the selected functions are consumed.

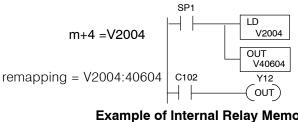


Using the remapping method, the panel configuration will automatically consume 96 consecutive memory bits in PLC User Memory (this occurs when the base register address is configured with OP-WINEDIT). This is indicated by the arrow A. But since User Memory doesn't provide bit control, the User Memory will need to be remapped with Internal Relay Memory. By remapping between User Memory and Internal Relay Memory, the Relay Memory needed will be consumed. There are two directions in which the ladder logic can be programmed to do the remapping between User Memory and Internal Relay Memory and Internal Relay Memory.

- For using the Pushbutton Status to control outputs, write ladder logic to map User Memory to Internal Relay Memory (arrow B). This affects the User Memory in the **m+4** location.
- For controlling all other functions of the panel, write the ladder logic to map Internal Relay Memory to User Memory (arrow C). This affects the User Memory in locations **m+0** through **m+3** and **m+5**.

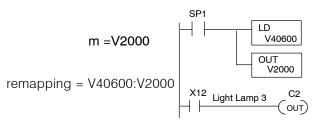
The two relay ladder examples of remapping below demonstrate the two types of remapping that can be used with this technique. Assume that V2000 was used as the base register address:

Example of User Memory being mapped to Internal Relay Memory

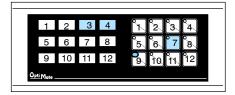


Here we are using SP1 to map V2004 to V40604. This consumes 16 relay bits, 12 of which are tied to the 12 pushbuttons of the panel. By pressing Pushbutton 3, you affect the status of the third relay in V40604 which is C102. In turn, C102 will control output Y12.

Example of Internal Relay Memory being mapped to User Memory

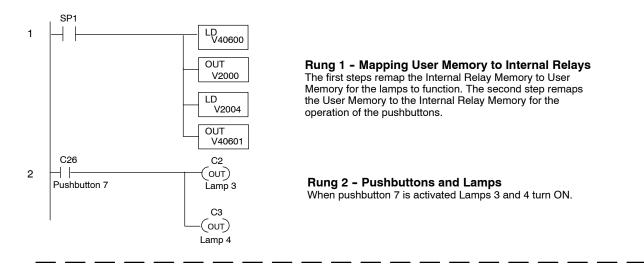


Here we are using SP1 to map V40600 to V2000. This consumes 16 relay bits, 12 of which are tied to the 12 Lamps of the panel. When a relay is ON, its corresponding Lamp is ON. By turning ON X12 with our ladder logic, we can thus turn on the Lamp corresponding to C2. C2 is bit 2 of the V40600 word and is tied to Lamp 3 through the mapping process. See your PLC User Manual for relay number assignments Using Ladder Logic with *Direct*LOGIC PLCs In the following examples, user memory will be remapped to internal relay memory. The internal relays of *Direct*LOGIC PLCs (DL05, DL06, DL105, DL205, DL350 and DL405) start at V40600. In the examples below, **V2000** has been used as the base address for a *Direct*LOGIC PLC, then **SP1** (always ON relay) is used in the ladder logic to perform the remapping. When using **SP1**, the remapping is performed on each scan, otherwise m+0 and m+1 would not be updated.



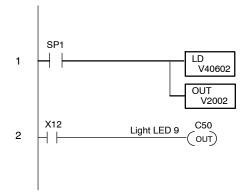
17 16	6 1	15	14														device number Internal Relay
0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	V40600	Indicator Lamp ON/OFF
0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	V40601	Button ON/OFF

MAPPING PUSHBUTTONS AND LAMPS



MAPPING LEDS

																			device number
																			Internal Relay
C)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	C)	V40602	Button LEDs ON/OFF

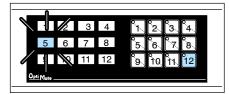


Rung 1 - Mapping Internal Relays to User Memory This step remaps the Internal Relay Memory to User Memory for the LEDs to function.

Rung 2 - LEDs

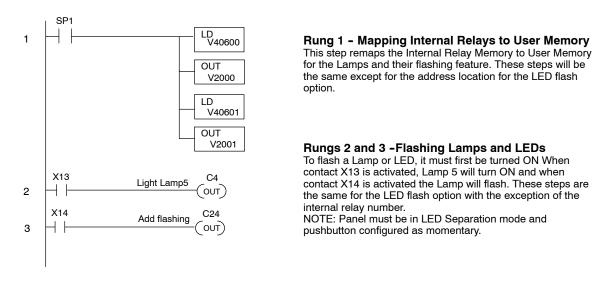
When contact X12 is ON, LED 9 turns ON NOTE: Panel must be in LED Separation mode and pushbutton configured as momentary.

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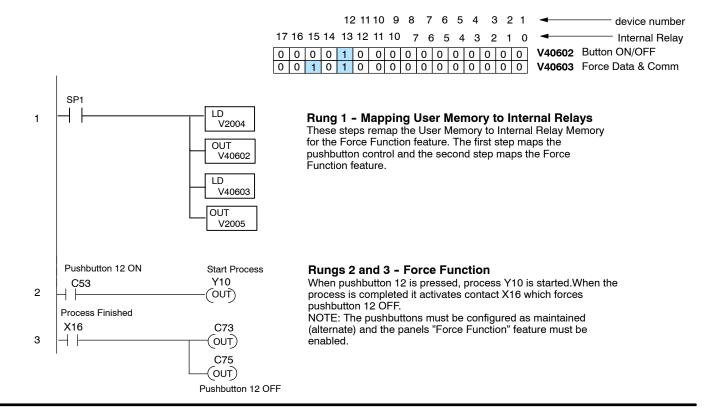


				12	2 11	10	9	8	7	6	5	4	Э	2	2 1	•	device number
17	16	15	14	13	12	11	10	7	6	5	4	3	2	1	0	-	Internal Relay
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	V40600	Indicator Lamp ON/OFF Indicator Lamp Flash
0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	V40601	Indicator Lamp Flash

MAPPING LAMPS AND FLASH FEATURE



MAPPING PUSHBUTTONS AND FORCE FUNCTION FEATURE

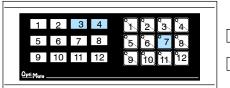


28

Using Ladder Logic with the DL305

C374

In the following examples, user memory will be remapped to internal relay memory in order to use the pushbutton status to control outputs. The internal relays of the DL305 family start at R16. In the examples below, R400 has been chosen as the base address for the DL305, then used normally closed C374 in the ladder logic to map it to R16. Using normally closed C374, the remapping is performed on each scan, otherwise m+0 and m+1 would not be updated.



DSTR R16

DOUT

DSTR

DOUT R20

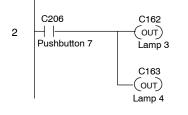
R400

R410

12 11 10 9	8 7 6	6543	21 ┥	device number
				 internal relay number
0000000000 R17	0 0 0	0 1 1	0 0 R 1	6 Indicator Light ON/OFF
0000000000 R21	0 1 0	0 0 0	0 0 R 2	20 Button ON/OFF

MAPPING PUSHBUTTONS AND LAMPS

Rung 1 - Mapping User Memory to Internal Relays The first steps remap the Internal Relay Memory to User Memory for the lamps to function. The second step remaps the User Memory to the Internal Relay Memory for the operation of the pushbuttons.

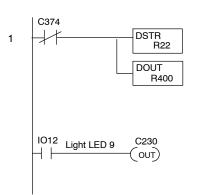


Rung 2 - Pushbuttons and Lamps

When pushbutton 7 is activated Lamps 3 and 4 turn ON.

MAPPING LEDS

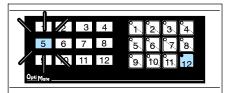
12 11 10 9	8	76	54	321	 device number
7 6 5 4 3 2 1 0	7	65	4 3	2 1 0	 internal relay number
0 0 0 0 0 0 0 1	R23 0	0 0	0 0	0 0 0	R22 Button LEDs ON/OFF



Rung 1 - Mapping Internal Relays to User Memory This step remaps the Internal Relay Memory to User Memory

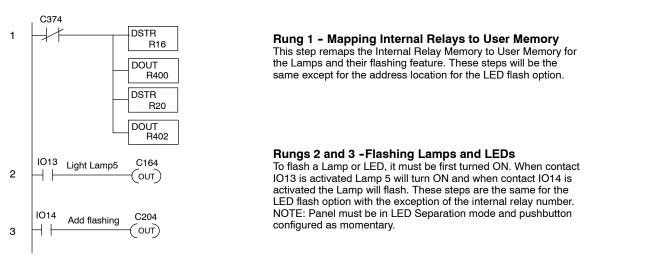
for the LEDs to function.

Rung 2 - LEDs When contact IO12 is ON, LED 9 turns ON NOTE: Panel must be in LED Separation mode and pushbutton configured as momentary.

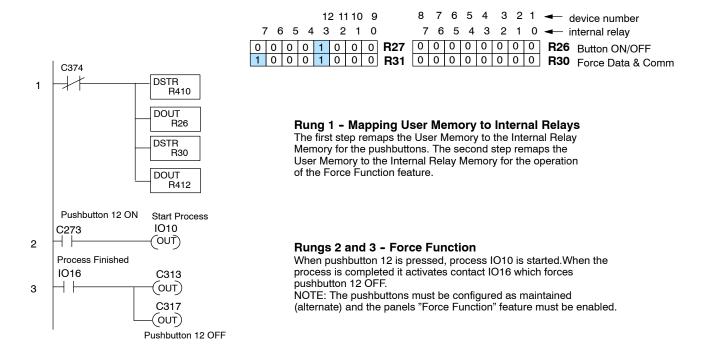


-		-	-	-	3	2	10 1	0		7	6	5	4	3	2	1	0	◄	device number internal relay number
0 0	0 0	0))	0 0	0 0	0 0	0 0	0 0	R17 R21	0 0	0 0	0 0	1	0 0	0 0	0 0	0 0	R16 R20	Indicator Lamp ON/OFF Indicator Lamp Flash

MAPPING LAMPS AND FLASH FEATURE

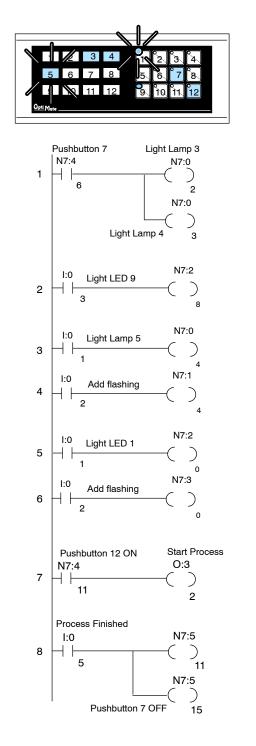


MAPPING PUSHBUTTONS AND FORCE FUNCTION FEATURE



Using the OP-1212 with an Allen-Bradley PLC

Using Ladder Logic with Allen-Bradley PLC Integer type of files can be mapped for the Allen-Bradley PLC when being used with the OP-1212. In the examples below, integer file registers starting at base address **N7:0** have been mapped. If you need more information on any of the features of the panel, refer to **Understanding the OP-1212 Panel** in this manual.



			12	11	10	9	8	7	6	5	4	3	2	1	device number
514	13	12	11	10	9	8	7	6	5	4	3	2	1	0	bit
0	0	0	0	0	0	0	0	0	0	1	1	1	0	0	N7:0 Indicator Lamp ON/OFF
0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	N7:1 Indicator Lamp Flash
0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	N7:2 Button LEDs ON/OFF
0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	N7:3 Button LEDs Flash
0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	N7:4 Button ON/OFF
0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	N7:5 Force Data & Comm
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Rung 1 - Pushbuttons and Lamps

When pushbutton 7 is activated Lamps 3 and 4 turn ON. Also, the LED in pushbutton 7 will turn ON if LED Separation is disabled and the pushbutton is configured as maintained.

Rung 2 - LEDs

When contact I:0/3 is ON, LED 9 turns ON NOTE: Panel must be in LED Separation mode and pushbutton configured as momentary.

Rungs 3 and 4 -Flashing Lamps

To flash a Lamp, it must be first turned ON When contact I:0/1 is activated Lamp 5 will turn ON and when contact I:0/2 is activated the Lamp will flash.

Rungs 5 and 6 - Flashing LEDs

To flash a LED, it must be first turned ON When contact I:0/1 is activated LED 1 will turn ON and when contact I:0/2 is activated the LED will flash. NOTE: Panel must be in LED Separation mode and pushbutton

configured as momentary.

Rungs 7 and 8 - Force Function

When pushbutton 12 is pressed, process O:3/2 is started.When the process is completed it activates contact I:0/5 which forces pushbutton 12 OFF. NOTE: The pushbuttons must be configured as maintained (alternate) and the panels "Force Function" feature must be enabled.