

Appendix A
Slice I/O
Worksheet

Master Module No. _____ Slave No. _____

Slice Slave Worksheet

Unit Address	Model Name	INPUT		OUTPUT	
		Input Address	No. Inputs	Output Address	No. Outputs
1					
2					
3					
4					
5					
6					
7					
8					
9					
10					
11					
12					
13					
14					
15					

Input Bit Start Address: _____ **V-Memory Address:V** _____

Total Input Points Consumed _____ **Input Points Used** _____

Output Bit Start Address: _____ **V-Memory Address:V** _____

Total Output Points Consumed _____ **Output Points Used** _____