



Errata Sheet

This Errata Sheet contains corrections or changes made after the publication of this manual.

Product Family: DL305 **Date:** August 2018
Manual Number: D3-350-M
Revision and Date: 2nd Edition, Rev. D; March 2010

Changes to Chapter 3. CPU Specifications and Operations

Page 3-6. Using Battery Backup; Enabling the Battery Backup

The ladder example shown to enable the backup battery is incorrect. Use the following example instead. Note: This example assumes that the original content of V7633 was a 0 (zero).



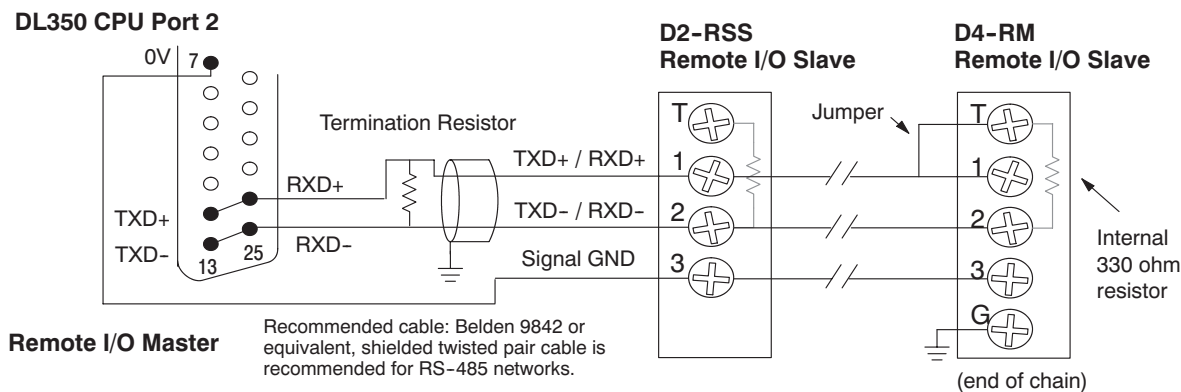
Changes to Chapter 4. System Design and Configuration

Page 4-15. I/O Configurations with a 10 Slot Local CPU Base

The four drawings on this page to the left of the bases showing jumper switch SW2 are mislabeled. They should say "700" and "100 EXP", not "700 EXP" and "100" as shown.

Page 4-18. CPU Specifications; Remote I/O Expansion; Configuring the CPU's Remote I/O Channel

Replace the remote I/O wiring diagram shown with this one.



Changes to Chapter 5. Standard RLL Instructions; Timer, Counter and Shift Register; Accumulating Timer (TMRA) and Accumulating Fast Timer (TMRAF)

Page 5-38. In the first paragraph, second sentence on this page, the maximum value for the TMRAF instruction reads 99999.99 (total of seven 9's). This is incorrect - it should be 999999.99 (total of eight 9's).



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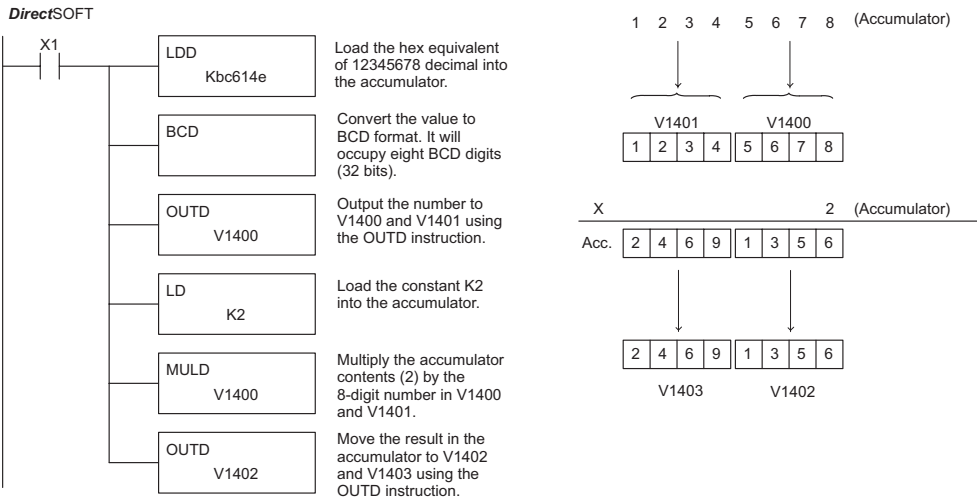
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Changes to Chapter 5. Standard RLL Instructions; Accumulator Logical Instructions; Compare Real Number (CMPR)

Page 5-76. In the ladder example, the contact reference is incorrect. It should be SP62 turning on output C1, not SP60.

Changes to Chapter 5. Standard RLL Instructions; Math Instructions; Multiply Double (MULD)

Page 5-84. The ladder example shown is incorrect. Replace it with the following example.



Handheld Programmer Keystrokes

| | | | | | | | | | | | | | | | | | | | | | | | | |
|------|-----|-------|---|-----|-----|-------|-----|------|------|---|---|---|---|------|---|---|---|-----|---|---|------|---|---|-----|
| \$ | STR | → | B | 1 | ENT | | | | | | | | | | | | | | | | | | | |
| SHFT | L | ANDST | D | 3 | D | 3 | → | PREV | SHFT | B | 1 | C | 2 | SHFT | G | 6 | B | 1 | E | 4 | SHFT | E | 4 | ENT |
| SHFT | B | 1 | C | 2 | D | 3 | ENT | | | | | | | | | | | | | | | | | |
| GX | OUT | SHFT | D | 3 | → | B | 1 | E | 4 | A | 0 | A | 0 | ENT | | | | | | | | | | |
| SHFT | L | ANDST | D | 3 | → | PREV | C | 2 | ENT | | | | | | | | | | | | | | | |
| SHFT | M | ORST | U | ISG | L | ANDST | D | 3 | → | B | 1 | E | 4 | A | 0 | A | 0 | ENT | | | | | | |
| GX | OUT | SHFT | D | 3 | → | B | 1 | E | 4 | A | 0 | C | 2 | ENT | | | | | | | | | | |

Changes to Chapter 5. Standard RLL Instructions; Network Instructions

Please change Step 1 on pages 5-137 and 5-139 (RX & WX Commands) to read as follows:

Step 1: - Load the slave address (0-90 BCD) into the first byte and the slot number of the master DCM (0-7) into the second byte of the second level of the accumulator stack. When using Port 2 of the CPU, the formatting should be Kf1xx where xx is the slave address (0-90 BCD).



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Changes to Chapter 8. PID Loop Operation

For more recent and complete information on PID loop operation, refer to Chapter 8 in the DL06 user manual (manual number D0-06USER-M).

Changes to Chapter 9. Maintenance and Troubleshooting

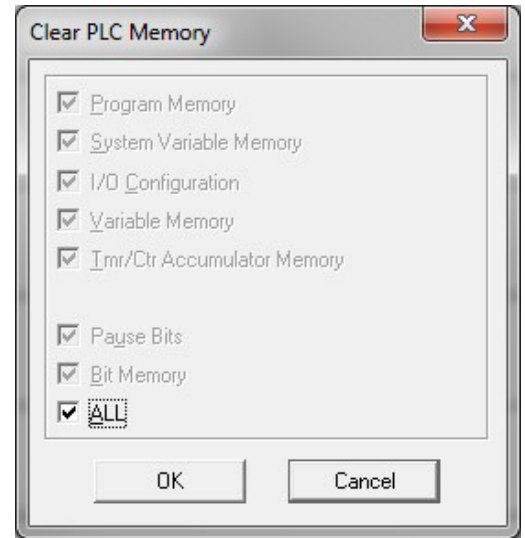
Page 9-25. Add the following to the end of this chapter (right after Regular Forcing with Direct Access):

Reset the PLC to Factory Defaults

NOTE: Resetting to factory defaults will not clear any password stored in the PLC.

Resetting a DirectLogic PLC to Factory Defaults is a two-step process. Be sure to have a verified backup of your program using “Save Project to Disk” from the File menu before performing this procedure. Please be aware that the program as well as any settings will be erased and not all settings are stored in the project. In particular you will need to write down any settings for Secondary Communications Ports and manually set the ports up after resetting the PLC to factory defaults.

Step 1 – While connected to the PLC with DirectSoft, go to the PLC menu and select; “Clear PLC Memory”. Check the “ALL” box at the bottom of the list and press “OK”.



Step 2 – While connected with DirectSoft, go the PLC menu and then to the “Setup” submenu and select “Initialize Scratch Pad”. Press “OK”.

NOTE: All configurable communications ports will be reset to factory default state. If you are connected via Port 2 or another configurable port, you may be disconnected when this operation is complete.

NOTE: Retentive ranges will be reset to the factory settings.

NOTE: Manually addressed IO will be reset to factory default settings.

The PLC has now been reset to factory defaults and you can proceed to program the PLC.

