Chapter 7

F2-04THM 4-Channel Thermocouple Input

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Module Specifications

The F2-04THM, 4-Channel Thermocouple Input Module provides the following features and benefits:

- Four thermocouple input channels with 16-bit voltage resolution or 0.1°F/°C temperature resolution.
- Automatically converts type E, J, K, R, S, T, B, N, or C thermocouple signals into direct temperature readings. No extra scaling or complex conversion is required.
- Temperature data can be expressed in °F or °C.
- Module can be configured as ±5V, ±156mV, 0–5V, 0–156mV input and will convert volts and millivolt signal levels into 16-bit digital (0–65535) values.
- Signal processing features include automatic cold junction compensation, thermocouple linearization, and digital filtering.
- The temperature calculation and linearization are based on data provided by the National Institute of Standards and Technology (NIST).
- Diagnostic features include detection of thermocouple burnout or disconnection.
Chapter 7: F2-04THM, 4-Channel Thermocouple Input

The following tables provide the specifications for the F2-04THM Analog Input Module. Review these specifications to make sure the module meets your application requirements.

### General Specifications

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of Channels</td>
<td>4, differential</td>
</tr>
<tr>
<td>Common Mode Range</td>
<td>±5VDC</td>
</tr>
<tr>
<td>Common Mode Rejection</td>
<td>90dB min. @ DC, 150dB min. @ 50/60Hz.</td>
</tr>
<tr>
<td>Input Impedance</td>
<td>1MΩ min.</td>
</tr>
<tr>
<td>Absolute Maximum Ratings</td>
<td>Fault-protected inputs to ±50VDC</td>
</tr>
<tr>
<td>Accuracy vs. Temperature</td>
<td>±5ppm/°C maximum; full scale calibration</td>
</tr>
<tr>
<td>(including maximum offset change)</td>
<td></td>
</tr>
<tr>
<td>Sampling Rate</td>
<td>All 4 channels: 1.4 seconds (*5.4 seconds)</td>
</tr>
<tr>
<td>PLC Update Rate</td>
<td>4 channels per scan max. (D2-240, D2-250−1, D2-260 and D2-262 CPU)</td>
</tr>
<tr>
<td></td>
<td>1 channel per scan, max. D2-230 CPU</td>
</tr>
<tr>
<td>Digital Inputs</td>
<td>16 binary data bits, 2 channel ID bits, 4 diagnostic bits</td>
</tr>
<tr>
<td></td>
<td>32 point (X) input module</td>
</tr>
<tr>
<td>Power Budget Requirement</td>
<td>80mA (*100mA) maximum, 5VDC (supplied by base)</td>
</tr>
<tr>
<td>External Power Supply</td>
<td>40 mA, 10-30 VDC (*60 mA, 18-26.4 VDC)</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>0–60°C (32–140°F)</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-2°C to 70°C (-4°F to 158°F)</td>
</tr>
<tr>
<td>Relative Humidity</td>
<td>5–95% (non-condensing)</td>
</tr>
<tr>
<td>Environmental air</td>
<td>No corrosive gases permitted</td>
</tr>
<tr>
<td>Vibration</td>
<td>MIL STD 810C 514.2</td>
</tr>
<tr>
<td>Shock</td>
<td>MIL STD 810C 516.2</td>
</tr>
<tr>
<td>Noise Immunity</td>
<td>NEMA ICS3-304</td>
</tr>
</tbody>
</table>

### Thermocouple Specifications

<table>
<thead>
<tr>
<th>Thermocouple Type</th>
<th>Input Ranges</th>
</tr>
</thead>
<tbody>
<tr>
<td>Type J</td>
<td>-190–760°C (-310–1400°F)</td>
</tr>
<tr>
<td>Type K</td>
<td>-150–1372°C (-238–2502°F)</td>
</tr>
<tr>
<td>Type E</td>
<td>-210–1000°C (-346–1832°F)</td>
</tr>
<tr>
<td>Type R</td>
<td>65–1768°C (149–3214°F)</td>
</tr>
<tr>
<td>*Type R Wide</td>
<td>65–1768°C (149–3214°F)</td>
</tr>
<tr>
<td>Type S</td>
<td>65–1768°C (149–3214°F)</td>
</tr>
<tr>
<td>Type T</td>
<td>-230–400°C (-382–752°F)</td>
</tr>
<tr>
<td>Type B</td>
<td>529–1820°C (984–3308°F)</td>
</tr>
<tr>
<td>Type N</td>
<td>-70–1300°C (-94–2372°F)</td>
</tr>
<tr>
<td>Type C</td>
<td>65–2320°C (149–4208°F)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Display Resolution</td>
<td>±0.1°C or ±0.1°F</td>
</tr>
<tr>
<td>Cold Junction Compensation</td>
<td>Automatic</td>
</tr>
<tr>
<td>Warm-Up Time</td>
<td>30 minutes typically ±1°C repeatability</td>
</tr>
<tr>
<td>Linearity Error (End to End)</td>
<td>±1°C maximum, ±0.5°C typical</td>
</tr>
<tr>
<td>Maximum Inaccuracy</td>
<td>±3°C (excluding thermocouple error)</td>
</tr>
</tbody>
</table>

**NOTE:** *R Wide range is available only on modules with date code 0410E2 and later.

**NOTE:** Values in parenthesis with an asterisk are for older modules with two circuit board design and date codes 0806E1 or previous. Values not in parenthesis are for single circuit board models with date code 0806E1 and above.
Module Calibration
The F2-04THM module requires no calibration. The module automatically calibrates every five seconds, which removes offset and gain errors. For each thermocouple type, the temperature calculation and linearization performed by the microprocessor is accurate to within 0.01°C.

Thermocouple Input Configuration Requirements
The F2-04THM temperature input module requires 32 discrete input points. The module can be installed in any slot of a DL205 system. The limitations on the number of analog modules are:

- For local and local expansion systems, the available power budget and number of discrete I/O points.
- For remote I/O systems, the available power budget and number of remote I/O points. Check the user manual for the particular model of CPU and I/O base being used for more information regarding power budget and number of local, local expansion or remote I/O points.
Special Placement Requirements (D2-230 and Remote I/O Bases)

It is important to examine the configuration if a D2-230 CPU is being used. As can be seen in the section on Writing the Control Program, V-memory locations are used to manage the analog data. If the module is placed in a slot so that the input points do not start on a V-memory boundary, the program instructions aren't able to access the data. This also applies when placing this module in a remote base using a D2-RSSS in the CPU slot.

Correct!

Data is correctly entered, so input points start on a V-memory boundary address as in the table graphic below.

Incorrect

Data is split over three locations, (see graphic below) so instructions cannot access data from a D2-230 CPU.

V-memory references required for a D2-230 CPU, the first input address assigned to the module must be one of the following X locations. The corresponding V-memory addresses for the X locations are shown below.
Chapter 7: F2-04THM, 4-Channel Thermocouple Input

Setting the Module Jumpers

Jumper Locations

Use the figures below to locate the single jumper (J9) and the bank of eight jumpers (J7) on the PC board. Notice that the PC board was re-designed starting with date code 0806E1 and the jumper locations changed; the functionality of the jumpers did not change. To prevent losing a jumper when it is removed, store it in its original location by sliding one of its sockets over a single pin. The following options can be selected by installing or removing the appropriate jumpers:

- Number of channels
- Input type
- Conversion unit
- Calibrate enable

Jumper locations for modules having date code prior to 0806E1.

Jumper locations for modules having date code 0806E1 and later.

Calibrate Enable

Locate the “Calibrate enable” jumper J9. The jumper comes from the factory with the jumper removed (the jumper is installed on one of the two pins only). Installing this jumper disables the thermocouple active burn-out detection circuitry, which enables a thermocouple calibrator to be attached to the module.

To be certain that the output of the thermocouple calibrator is within the 5V common mode voltage range of the module, connect the negative side of the differential voltage input channel to the 0V terminal, then connect the thermocouple calibrator to the differential inputs (for example, Ch 3+ and Ch 3).

For the voltage input ranges, this jumper is inactive and can be installed or removed with no effect on voltage input.
Selecting the Number of Channels

The top two J7 jumpers labeled CH+1 and CH+2 determine the number of channels that will be used. The table shows how to set the jumpers for channels 1 to 4. The module comes with both jumpers installed for four channel operation. For example, to select channels 1 to 3, leave the CH+2 jumper installed and remove the CH+1 jumper. Any unused channels are not processed. For example, if channels 1 to 3 are selected, channel 4 will not be active.

<table>
<thead>
<tr>
<th>Number of Channels</th>
<th>Jumper</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>– –</td>
</tr>
<tr>
<td>2</td>
<td>X –</td>
</tr>
<tr>
<td>3</td>
<td>– X</td>
</tr>
<tr>
<td>4</td>
<td>X X</td>
</tr>
</tbody>
</table>

Setting Input Type

The next four jumpers, Tc Type 0, Tc Type 1, Tc Type 2, and Tc Type 3, must be set to match either the type of thermocouple being used or the input voltage level. Since the module can be used with many types of thermocouples, use the table below to determine the proper settings for the thermocouple being used.

The module comes from the factory with all four jumpers installed for use with a J type thermocouple. To use a K type thermocouple, remove the jumper labeled Tc Type 0.

NOTE: All channels of the module must be the same thermocouple type or voltage range.

X = Jumper installed, and blank space = Jumper removed.

<table>
<thead>
<tr>
<th>Thermocouple/ Voltage Inputs</th>
<th>Tc Type 0</th>
<th>Tc Type 1</th>
<th>Tc Type 2</th>
<th>Tc Type 3</th>
</tr>
</thead>
<tbody>
<tr>
<td>J</td>
<td>X</td>
<td>X</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>K</td>
<td>–</td>
<td>X</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>E</td>
<td>X</td>
<td>–</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>R</td>
<td>–</td>
<td>–</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>R Wide*</td>
<td>–</td>
<td>X</td>
<td>–</td>
<td>–</td>
</tr>
<tr>
<td>S</td>
<td>X</td>
<td>X</td>
<td>–</td>
<td>X</td>
</tr>
<tr>
<td>T</td>
<td>–</td>
<td>X</td>
<td>–</td>
<td>X</td>
</tr>
<tr>
<td>B</td>
<td>X</td>
<td>–</td>
<td>–</td>
<td>X</td>
</tr>
<tr>
<td>N</td>
<td>–</td>
<td>–</td>
<td>–</td>
<td>X</td>
</tr>
<tr>
<td>C</td>
<td>X</td>
<td>X</td>
<td>X</td>
<td>–</td>
</tr>
<tr>
<td>0–5V</td>
<td>–</td>
<td>X</td>
<td>X</td>
<td>–</td>
</tr>
<tr>
<td>±5V</td>
<td>X</td>
<td>–</td>
<td>X</td>
<td>–</td>
</tr>
<tr>
<td>0–156 mV</td>
<td>–</td>
<td>–</td>
<td>X</td>
<td>–</td>
</tr>
<tr>
<td>±156 mV</td>
<td>X</td>
<td>X</td>
<td>–</td>
<td>–</td>
</tr>
</tbody>
</table>

*NOTE: R Wide is only available on modules with date code 0410E2 and later.
Selecting the Conversion Units

Use the last two jumpers, Units-0 and Units-1, to set the conversion unit used for either thermocouple or voltage inputs. The options are magnitude plus sign or 2’s complement, plus Fahrenheit or Celsius for thermocouples. See the next two sections for jumper settings when using either thermocouple or voltage inputs.

Thermocouple Conversion Units

All thermocouple types are converted into a direct temperature reading in either Fahrenheit or Celsius. The data contains one implied decimal place. For example, a value in V-memory of 1002 would be 100.2°F or °C.

For thermocouple ranges which include negative temperatures (J,E,K,T,N), the display resolution is from -3276.7 to +3276.7. For positive-only thermocouple ranges (R,S,B,C), the display resolution is 0 – 6553.5.

Negative temperatures can be represented in either 2’s complement or magnitude plus sign form. If the temperature is negative, the most significant bit in the V-memory location is set (X17, if the starting address for the module is X0).

The 2’s complement data format may be required to correctly display bipolar data on some operator interfaces. This data format could also be used to simplify averaging a bipolar signal.

To view this data format in DirectSoft, select Signed Decimal.

For unipolar thermocouple ranges (R,S,B,C), it does not matter if magnitude plus sign or 2’s complement is selected.

Use the table to select settings. The module comes with both jumpers installed for magnitude plus sign conversion in Fahrenheit. For example, remove the Units-0 jumper and leave the Units-1 jumper installed for magnitude plus sign conversion in Celsius.

X = Jumper installed, and
blank space = Jumper removed.

Voltage Conversion Units

The bipolar voltage input ranges, 5V or 156mV (see previous page for 5V and 156mV settings), may be converted to a 15-bit magnitude plus sign or a 16-bit 2’s complement value.

Use the table to select settings. The module comes with both jumpers installed for magnitude plus sign conversion. Remove the Units-1 jumper and leave the Units-0 jumper installed for 2’s complement conversion.

X = Jumper installed, and
blank space = Jumper removed.

NOTE: When selecting a Unipolar Voltage mode (0-5V, 0-156mV), BCD data type will not give a correct reading. Decimal data type should always be used for Unipolar Voltage modes.
Connecting the Field Wiring

Wiring Guidelines
Your company may have guidelines for wiring and cable installation. If so, check the guidelines before beginning the installation. Here are some general things to consider:

• Use the shortest wiring route whenever possible.

• Use shielded wiring and ground the shield at the transmitter source. Do not ground the shield at both the module and the source.

• Do not run the signal wiring next to large motors, high current switches, or transformers. This may cause noise problems.

• Route the wiring through an approved cable housing to minimize the risk of accidental damage. Check local and national codes to choose the correct method for your application.

User Power Supply Requirements
The F2-04THM module requires at least one field-side power supply. The same or separate power sources can be used for the 0–5 V or 0–156 mV transmitter voltage supply. The module requires 10–30 VDC, at 40mA, from the external power supply.

The DL205 AC bases have a built-in 24VDC power supply that provide up to 300mA of current. This can be used instead of a separate supply. Check the power budget to be safe.

It is desirable in some situations to power the transmitters separately in a location remote from the PLC. This will work as long as the transmitter supply meets the voltage and current requirements, and the transmitter’s minus (-) side and the module supply’s minus (-) side are connected together.

WARNING: If the internal 24VDC power budget is exceeded, it may cause unpredictable system operation that can lead to a risk of personal injury or equipment damage.

The DL205 base has a switching type power supply. As a result of switching, noise may cause some instability into the analog input data if the base power supply is used. If this is unacceptable, try one of the following:

1. Use a separate linear power supply.

2. Connect the 24VDC common to the frame ground, which is the screw terminal marked “G” on the base.

Unused temperature inputs should be shorted together and connected to common.
Thermocouples
Use shielded thermocouples whenever possible to minimize the presence of noise on the thermocouple wire. Ground the shield wire at one end only. For grounded thermocouples, connect the shield at the sensor end. For ungrounded thermocouples, connect the shield to the 0V (common) terminal.

Grounded Thermocouple Assembly
A grounded thermocouple provides better response time than an ungrounded thermocouple because the tip of the thermocouple junction is in direct contact with the protective case.

Ungrounded Thermocouple Assembly
An ungrounded thermocouple is electrically isolated from the protective case. If the case is electrically grounded it provides a low-impedance path for electrical noise to travel. The ungrounded thermocouple provides a more stable and accurate measurement in a noisy environment.

Exposed Grounded Thermocouple
The thermocouple does not have a protective case and is directly connected to a device with a higher potential. Grounding the thermocouple assures that the thermocouple remains within the common mode specifications. Because a thermocouple is essentially a wire, it provides a low-impedance path for electrical noise. The noise filter has a response of >100dB @ 50/60 Hz.

WARNING: A thermocouple can become shorted to a high voltage potential. Because common terminals are internally connected together, whatever voltage potential exists on one thermocouple will exist on the other channels.

Ambient Variations in Temperature
The F2-04THM module has been designed to operate within the ambient temperature range of 0 to 60°C.

The cold junction compensation is calibrated to operate in a still-air environment. If the module is used in an application that has forced convection cooling, an error of 2 to 3°C may be introduced. To compensate for this, ladder logic can be used to correct the values.

When configuring the system design it is best to locate any heat-producing devices above and away from the PLC chassis because the heat will affect the temperature readings. For example, heat introduced at one end of the terminal block can cause a channel-to-channel variation.

When exposing the F2-04THM module to abrupt ambient temperature changes it will take several minutes for the cold junction compensation and terminal block to stabilize. Errors introduced by abrupt ambient temperature changes will be less than 4°C.
Wiring Diagrams
Use the following diagrams to connect the field wiring.

Thermocouple Input Wiring Diagram

![Thermocouple Input Wiring Diagram]

**NOTE:** Terminate shields at the respective signal source. Also, connect unused channels to a common terminal (0V, CH4+, CH4).

Voltage Input Wiring Diagram

![Voltage Input Wiring Diagram]

**NOTE:** Connect unused channels to a common terminal (0V, CH4+, CH4). Also, when using 0-156 mV and 5V ranges, connect (-) or 0V terminals (CH1, CH2, CH3, CH4) to 0V module supply terminal to ensure common mode acceptance.
With grounded thermocouples, take precautions to prevent having a voltage potential between thermocouple tips. A voltage of 1.25V or greater between tips will skew measurements.
Module Operation

Before beginning to write the control program, it is important to take a few minutes to understand how the module processes the analog signals.

Channel Scanning Sequence (Multiplexing) for a D2-230 CPU

The F2-04THM module can supply different amounts of data per scan, depending on the type of CPU being used. The D2-230 can obtain one channel of data per CPU scan. Since there are four channels, it can take up to four scans to get data for all channels. Once all channels have been scanned the process starts over with channel 1. Unused channels are not processed, so if only two channels are selected, each channel will be updated every other scan. The multiplexing method can also be used for the D2-240, D2-250-1, D2-260 or D2-262 CPUs.
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Channel Scanning Sequence (Pointer Method) for D2-240, D2-250-1, D2-260 and D2-262 CPUs

If a D2-240, a D2-250-1, a D2-260 or a D2-262 CPU is being used, all four channels of input data can be captured in one scan. This is because the D2-240, D2-250-1, D2-260 and D2-262 CPUs support special V-memory locations that are used to manage the data transfer. This is discussed in more detail in the next section on Writing the Control Program.

Analog Module Updates

Even though the channel updates to the CPU are synchronous with the CPU scan, the module asynchronously monitors the analog transmitter signal and converts the signal to a 16-bit binary representation. This enables the module to continuously provide accurate measurements without slowing down the discrete control logic in the RLL program.

The time required to sense the temperature and copy the value to V-memory is 1.4 seconds for a single board design module (5.4 seconds for a two board design module) plus 1 scan time maximum.
Chapter 7: F2-04THM, 4-Channel Thermocouple Input

Writing the Control Program

Reading Values Pointer Method and Multiplexing

There are two methods of reading values:

- Pointer method
- Multiplexing

The multiplexing method must be used with a D2-230 CPU. The multiplexing method must also be used with remote I/O modules (the pointer method will not work). Either method can be used with the D2-240, D2-250-1, D2-260 and D2-262 CPUs, but for ease of programming it is highly recommended to use the pointer method.

Pointer Method for the D2-240, D2-250-1, D2-260 and D2-262 CPUs

The CPU has special V-memory locations (shown in tables on the following page) assigned to each base slot that greatly simplifies the programming requirements. These V-memory locations allow you to:

- Specify the data format
- Specify the number of channels to scan
- Specify the storage locations

The example program shows how to setup these locations. Place this rung anywhere in the ladder program, or in the initial stage if stage programming instructions are used. This is all that is required to read the data into V-memory locations. Once the data is in V-memory, math instructions can be used on the data, compare the data against preset values, etc. V2000 is used in the example, but any user V-memory location can be used. The module is installed in slot 2 for the examples. Use the V-memory locations shown in the application. The pointer method automatically converts values to BCD.

NOTE: D2-240 CPUs with firmware release version 2.5 or later and D2-250 CPUs with firmware release version 1.06 or later support this method. Use the D2-230 multiplexing example if the firmware revision is earlier.

```
LD K0400
LDA O2000
OUT V7662
- or -
LD K8400
OUT V7662
```

Loads a constant that specifies the number of channels to scan and the data format. The upper byte, most significant nibble (MSN) selects the data format (0=BCD, 8=Binary), the LSN selects the number of channels (1, 2, 3, or 4).

The binary format is used for displaying data on some operator interfaces. The DL230/240 CPUs do not support binary math functions, whereas the DL250 does.

```
LD K0400
LDA O2000
OUT V7672
```

Special V-memory location assigned to slot 2 that contains the number of channels to scan.

This loads an octal value for the first V-memory location that will be used to store the incoming data. For example, the O2000 entered here would designate the following addresses:


The octal address (O2000) is stored here. V7672 is assigned to slot 2 and acts as a pointer, which means the CPU will use the octal value in this location to determine exactly where to store the incoming data.
The following tables show the special V-memory locations used by the D2-240, D2-250-1, D2-260 and D2-262 for the CPU base and local expansion base I/O slots. Slot 0 (zero) is the module next to the CPU or D2-CM module. Slot 1 is the module two places from the CPU or D2-CM, and so on. Remember, the CPU only examines the pointer values at these locations after a mode transition. Also, if the D2-230 (multiplexing) method is used, verify that these addresses in the CPU are 0 (zero).

The Table below applies to the D2-240, D2-250-1, D2-260 and D2-262 CPU base.

<table>
<thead>
<tr>
<th>CPU Base: Analog Input Module Slot-Dependent V-memory Locations</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Slot</strong></td>
</tr>
<tr>
<td><strong>No. of Channels</strong></td>
</tr>
<tr>
<td><strong>Storage Pointer</strong></td>
</tr>
</tbody>
</table>

The Table below applies to the D2-250-1, D2-260 and D2-262 CPU base 1.

<table>
<thead>
<tr>
<th>Expansion Base D2-CM #1: Analog Input Module Slot-Dependent V-memory Locations</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Slot</strong></td>
</tr>
<tr>
<td><strong>No. of Channels</strong></td>
</tr>
<tr>
<td><strong>Storage Pointer</strong></td>
</tr>
</tbody>
</table>

The Table below applies to the D2-250-1, D2-260 and D2-262 CPU base 2.

<table>
<thead>
<tr>
<th>Expansion Base D2-CM #2: Analog Input Module Slot-Dependent V-memory Locations</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Slot</strong></td>
</tr>
<tr>
<td><strong>No. of Channels</strong></td>
</tr>
<tr>
<td><strong>Storage Pointer</strong></td>
</tr>
</tbody>
</table>

The Table below applies to the D2-250-1, D2-260 and D2-262 CPU base 3.

<table>
<thead>
<tr>
<th>Expansion Base D2-CM #3: Analog Input Module Slot-Dependent V-memory Locations</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Slot</strong></td>
</tr>
<tr>
<td><strong>No. of Channels</strong></td>
</tr>
<tr>
<td><strong>Storage Pointer</strong></td>
</tr>
</tbody>
</table>

The Table below applies to the D2-250-1, D2-260 and D2-262 CPU base 4.

<table>
<thead>
<tr>
<th>Expansion Base D2-CM #4: Analog Input Module Slot-Dependent V-memory Locations</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Slot</strong></td>
</tr>
<tr>
<td><strong>No. of Channels</strong></td>
</tr>
<tr>
<td><strong>Storage Pointer</strong></td>
</tr>
</tbody>
</table>
Negative Temperature Readings with Magnitude Plus Sign (Pointer Method) for D2-240, D2-250-1, D2-260 and D2-262 CPUs

With bipolar ranges, some additional logic will be needed to determine whether the value being returned represents a positive voltage or a negative voltage. For example, the direction for a motor might need to be known. There is a solution for this:

- If bipolar ranges are used and a value greater than or equal to \(8000_{\text{hex}}\) is obtained, the value is negative.
- If a value less than or equal to \(7FFF_{\text{hex}}\) is obtained, then the value is positive.

The sign bit is the most significant bit, which combines \(8000_{\text{hex}}\) to the data value. If the value is greater than or equal to \(8000_{\text{hex}}\), only the most significant bit and the active channel bits need to be masked to determine the actual data value.

**NOTE:** D2-240 CPUs with firmware release version 2.5 or later and D2-250 CPUs with firmware release version 1.06 or later support this method. Use the D2-230 multiplexing example if your firmware is an earlier version.

The following two programs on this page and the next page show how this can be accomplished. The first example uses magnitude plus sign (binary) and the second example uses magnitude plus sign (BCD). The examples only show two channels.

It is good to know when a value is negative, so these rungs should be placed before any other operations that use the data, such as math instructions, scaling operations, etc. Also, if stage programming instructions are being used, these rungs should be in a stage that is always active.

**NOTE:** This logic is only needed for each channel that is using bipolar input signals.

**Magnitude Plus Sign (Binary)**

**Check Channel 1**

<table>
<thead>
<tr>
<th>SP1</th>
</tr>
</thead>
<tbody>
<tr>
<td>LD V2000</td>
</tr>
<tr>
<td>ANDD K7FFF</td>
</tr>
<tr>
<td>OUT V2010</td>
</tr>
<tr>
<td>V2000 K8000 C1 (OUT)</td>
</tr>
</tbody>
</table>

Check Channel 1 data is negative when C1 is on (a value of \(-1.0\) reads as 8010, \(-2.0\) is 8020, etc.).

Load channel 1 data from V-memory into the accumulator. Contact SP1 is always on.

This instruction masks the sign bit of the binary data, if it is set. Without this step, negative values will not be correct so do not forget to include it.

Put the actual signal value in V2010. Now you can use the data normally.

**Check Channel 2**

<table>
<thead>
<tr>
<th>SP1</th>
</tr>
</thead>
<tbody>
<tr>
<td>LD V2002</td>
</tr>
<tr>
<td>ANDD K7FFF</td>
</tr>
<tr>
<td>OUT V2012</td>
</tr>
<tr>
<td>V2002 K8000 C2 (OUT)</td>
</tr>
</tbody>
</table>

Channel 2 data is negative when C2 is on (a value of \(-1.0\) reads as 8010, \(-2.0\) is 8020, etc.).

Load channel 2 from V-memory into the accumulator. Contact SP1 is always on.

This instruction masks the sign bit of the binary data, if it is set. Without this step, negative values will not be correct so do not forget to include it.

Put the actual signal value in V2012. Now you can use the data normally.
Magnitude Plus Sign (BCD)

Check Channel 1

Load channel 1 data from V-memory into the accumulator. Remember, the data can be negative. Contact SP1 is always on.

This instruction masks the sign bit of the BCD data, if it is set. Without this step, negative values will not be correct so do not forget to include it.

Put the actual signal value in V2010. Now you can use the data normally.

Check Channel 2

Load channel 2 from V-memory into the accumulator. Remember, the data can be negative. Contact SP1 is always on.

This instruction masks the sign bit of the BCD data, if it is set. Without this step, negative values will not be correct so do not forget to include it.

Put the actual signal value in V2012. Now you can use the data normally.
Negative Temperatures 2’s Complement (Binary/Pointer Method) for D2-240, D2-250-1, D2-260 and D2-262 CPUs

The 2’s complement mode is used for negative temperature display purposes, while at the same time using the magnitude plus sign of the temperature in a control program. The DirectSoft element Signed Decimal is used to display negative numbers in 2’s complement form. To find the absolute value of a negative number in 2’s complement, invert the number and add 1 as shown in the following example:

```
LD V2000
INV
ADDB K1
OUT V2010
```

Load negative value into the accumulator so we can convert it to a positive value.

Invert the binary pattern in the accumulator.

Add 1.

Save Channel 1 data at V2010.

Repeat for other channels as required.

Understanding the Input Assignments (Multiplexing Ladder Only)

Remember that the F2-04THM module appears as a 32-point discrete input module to the CPU. Use these points to obtain:

- An indication of which channel is active
- The digital representation of the analog signal
- Module diagnostic information

Since all input points are automatically mapped into V-memory, it is easy to determine the location of the data word that will be assigned to the module.
When a D2-230 CPU is used, the input points must start on a V-memory boundary. To use the V-memory references required for a D2-230 CPU, refer to the table below. The first input address assigned to a module must be one of the X inputs shown. The table also shows the V-memory addresses that correspond to these X inputs.

<table>
<thead>
<tr>
<th>X</th>
<th>V40400</th>
<th>V40401</th>
<th>V40402</th>
<th>V40403</th>
<th>V40404</th>
<th>V40405</th>
<th>V40406</th>
<th>V40407</th>
</tr>
</thead>
<tbody>
<tr>
<td>X0</td>
<td>V40400</td>
<td>V40401</td>
<td>V40402</td>
<td>V40403</td>
<td>V40404</td>
<td>V40405</td>
<td>V40406</td>
<td>V40407</td>
</tr>
</tbody>
</table>

**Analog Data Bits**
The first 16 bits represent the analog data in binary format.

<table>
<thead>
<tr>
<th>Bit</th>
<th>Value</th>
<th>Bit</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>8</td>
<td>256</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>9</td>
<td>512</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>10</td>
<td>1024</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>11</td>
<td>2048</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>12</td>
<td>4096</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>13</td>
<td>8192</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>14</td>
<td>16384</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>15</td>
<td>32768</td>
<td></td>
</tr>
</tbody>
</table>

**Active Channel Bits**
The active channel bits represent the multiplexed channel selections in binary format.

<table>
<thead>
<tr>
<th>Bit 1</th>
<th>Bit 0</th>
<th>Channel</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>0</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>4</td>
</tr>
</tbody>
</table>

**Broken Transmitter Bits (Pointer and Multiplexing Ladder Methods)**
The broken transmitter bits are on when the corresponding RTD is open.

<table>
<thead>
<tr>
<th>Bit</th>
<th>Channel</th>
</tr>
</thead>
<tbody>
<tr>
<td>8</td>
<td>1</td>
</tr>
<tr>
<td>9</td>
<td>2</td>
</tr>
<tr>
<td>10</td>
<td>3</td>
</tr>
<tr>
<td>11</td>
<td>4</td>
</tr>
</tbody>
</table>
Reading Magnitude Plus Sign Values (Multiplexing)

The D2-230 CPU does not have the special V-memory locations that allows for automatic management of the data transfer. Since all channels are multiplexed into a single data word, the control program must be set up to determine which channel is being read. Since the module appears as X input points to the CPU, it is very easy to use the active channel status bits to determine which channel is being monitored.

**NOTE:** D2-230 CPUs with firmware release version 1.6 or later required for multiplexing ladder.

```
LD V40401
ANDD K7FFF
SP1

OUT V2000

C0 (RST)
X37
C0 (SET)

When X40, X41, and X50 are off, channel 1 data is stored in V2000. C0 is reset to indicate that channel 1's value is positive.

If X37 is on, the data value represents a negative temperature. C0 is set to indicate that channel 1's value is negative.

```

```
OUT V2001

C1 (RST)
X37
C1 (SET)

When X40 is on and X41 and X51 are off, channel 2 data is stored in V2001. C1 is reset to indicate that channel 2's value is positive.

If X37 is on, the data value represents a negative temperature. C1 is set to indicate that channel 2's value is negative.

```

```
OUT V2002

C2 (RST)
X37
C2 (SET)

When X40 and X52 are off and X41 is on, channel 3 data is stored in V2002. C2 is reset to indicate that channel 3's value is positive.

If X37 is on, then the data value represents a negative temperature. C2 is set to indicate that channel 3's value is negative.

```

```
OUT V2003

C3 (RST)
X37
C3 (SET)

When both X40 and X41 are on and X53 is off, channel 4 data is stored in V2003. C3 is reset to indicate that channel 4's value is positive.

If X37 is on, the data value represents a negative temperature. C3 is set to indicate that channel 4's value is negative.

```

Load the complete data word into the accumulator. The V-memory location depends on the I/O configuration. See Appendix A for the memory map.

This instruction masks the sign bit. Without this, the values used will not be correct so do not forget to include it.
Chapter 7: F2-04THM, 4-Channel Thermocouple Input

**Reading 2’s Complement Values (Multiplexing)**

The D2-230 CPU does not have the special V-memory locations that allows for automatic management of the data transfer. Since all channels are multiplexed into a single data word, the control program must be set up to determine which channel is being read. Since the module appears as X input points to the CPU, it is very easy to use the active channel status bits to determine which channel is being monitored. The 2’s complement data format may be required to correctly display bipolar data on some operator interfaces. This data format could also be used to simplify averaging a bipolar signal. To view this data format in *DirectSOFT*, select Signed Decimal.

**Load Data**

<table>
<thead>
<tr>
<th>SP1</th>
<th>LD V40401</th>
<th>ANDD K7FFF</th>
</tr>
</thead>
</table>

Loads the complete data word into the accumulator. The V-memory location depends on the I/O configuration. This instruction masks the channel sign bit.

**Store Channel 1**

X40 X41 X50 | OUT V2000

When X40, X41 and X50 are off, channel 1 data is stored in V2000.

**Store Channel 2**

X40 X41 X51 | OUT V2001

When X40 is on and X41 and X51 are off, channel 2 data is stored in V2001.

**Store Channel 3**

X40 X41 X52 | OUT V2002

When X40 and X52 are off and X41 is on, channel 3 data is stored in V2002.

**Store Channel 4**

X40 X41 X53 | OUT V2003

When both X40 and X41 are on and X53 is off, channel 4 data is stored in V2003.

**Scaling the Input Data**

No scaling of the input temperature is required. The readings directly reflect the actual temperatures. For example: a reading of 8482 is 848.2°C, a reading of 16386 is -0.2°C (magnitude plus sign) and a reading of 32770 is -0.2°C (2’s complement).
Module Resolution 16-Bit (Unipolar Voltage Input)

Unipolar analog signals are converted into 65536 counts ranging from 0–65535 (2^{16}). For example, with a 0–156mV signal range, 78mV would be 32767. A value of 65535 represents the upper limit of the range.

\[ \text{Unipolar Resolution} = \frac{H - L}{65535} \]

Module Resolution 15-Bit Plus Sign (Bipolar Voltage Input)

The module has 16-bit unipolar or 15-bit + sign bipolar resolution. Bipolar analog signals are converted into 32768 counts ranging from 0–32767 (2^{15}). For example, with a -156mV–156mV signal range, 156mV would be 32767. The bipolar ranges utilize a sign bit to provide 16-bit resolution. A value of 32767 can represent the upper limit of either side of the range. Use the sign bit to determine negative values.

\[ \text{Bipolar Resolution} = \frac{H - L}{32767} \]
Analog and Digital Value Conversions

Sometimes it is useful to be able to quickly convert between the signal levels and the digital values. This is especially helpful during startup or troubleshooting. This module does not operate like other versions of analog input modules. The bipolar ranges use 0–32767 for both positive and negative voltages. The sign bit allows this and it actually provides better resolution than those modules that do not offer a sign bit. The following table provides formulas to make this conversion easier.

<table>
<thead>
<tr>
<th>Range</th>
<th>If the digital value is known</th>
<th>If the analog signal level is known.</th>
</tr>
</thead>
<tbody>
<tr>
<td>0–5 V</td>
<td>A = ( \frac{5D}{65535} )</td>
<td>D = 65535 (A) 5</td>
</tr>
<tr>
<td>0–156.25 mV</td>
<td>A = ( \frac{0.15625D}{65535} )</td>
<td>D = 65535 (A) 0.15625</td>
</tr>
<tr>
<td>±5V</td>
<td>A = ( \frac{10D}{65535} )</td>
<td>D = 65535 (A) 10</td>
</tr>
<tr>
<td>±156.25 mV</td>
<td>A = ( \frac{0.3125D}{65535} )</td>
<td>D = 65535 (A) 0.3125</td>
</tr>
</tbody>
</table>

For example, if the ±5V range is used and the signal is measured at 2.5V, use the following formula to determine the digital value that is stored in the V-memory location that contains the data.

\[ D = \frac{65535}{10} (A) \]
\[ D = \frac{65535}{10} (2.5V) \]
\[ D = 6553.5 \ (2.5) \]
\[ D = 16383.75 \]
Filtering Input Noise (D2-250-1, D2-260 and D2-262 CPUs Only)

Add the following logic to filter and smooth analog input noise in D2-250-1, D2-260 and D2-262 CPUs. This is especially useful when using PID loops. Noise can be generated by the field device and/or induced by field wiring.

In the following example, the analog value in BCD is first converted to a binary number. Memory location V1400 is the designated workspace in this example. The MULR instruction is the filter factor, which can be from 0.1–0.9. The example uses 0.2. Using a smaller filter factor increases filtering. A higher precision value can be used, but it is not generally needed. The filtered value is then converted back to binary and then to BCD. The filtered value is stored in location V1402 for use in the application program or a PID loop.

**NOTE:** Please review intelligent instructions (IBox) in Chapter 5, which simplify this and other functions. The IBox instructions are supported by the D2-250-1, D2-260 and D2-262.

**NOTE:** Be careful not to do a multiple number conversion on a value. For example, if the pointer method is used to get the analog value, it is in BCD and must be converted to binary. However, if the conventional method of reading analog is used and the first 15 bits are masked, the value is already in binary and no conversion is needed. Also, if the conventional method is used, change the LDD V2000 instruction to LD V2000.

```
LDD V2000
BIN
BTOR
SUBR V1400
MULR R0.2
ADDR V1400
OUTD V1400
RTOB
BCD
OUTD V1402
```

- **LDD V2000**: Loads the analog signal, which is a BCD value and has been loaded from V-memory location V2000, into the accumulator. Contact SP1 is always on.
- **BIN**: Converts the BCD value in the accumulator to binary. Remember, this instruction is not needed if the analog value is originally brought in as a binary number.
- **BTOR**: Converts the binary value in the accumulator to a real number.
- **SUBR V1400**: Subtracts the real number stored in location V1400 from the real number in the accumulator, and stores the result in the accumulator. V1400 is the designated workspace in this example.
- **MULR R0.2**: Multiplies the real number in the accumulator by 0.2 (the filter factor), and stores the result in the accumulator. This is the filtered value.
- **ADDR V1400**: Adds the real number stored in location V1400 to the real number filtered value in the accumulator, and stores the result in the accumulator.
- **OUTD V1400**: Copies the value in the accumulator to location V1400.
- **RTOB**: Converts the real number in the accumulator to a binary value, and stores the result in the accumulator.
- **BCD**: Converts the binary value in the accumulator to a BCD number.
- **OUTD V1402**: Loads the BCD number filtered value from the accumulator into location V1402 to use in your application or PID loop.