

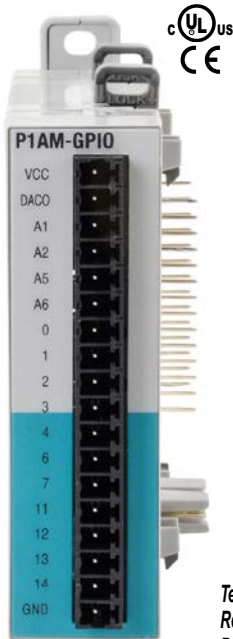
Shields

P1AM-GPIO \$61.00

Header Pin Breakout Module

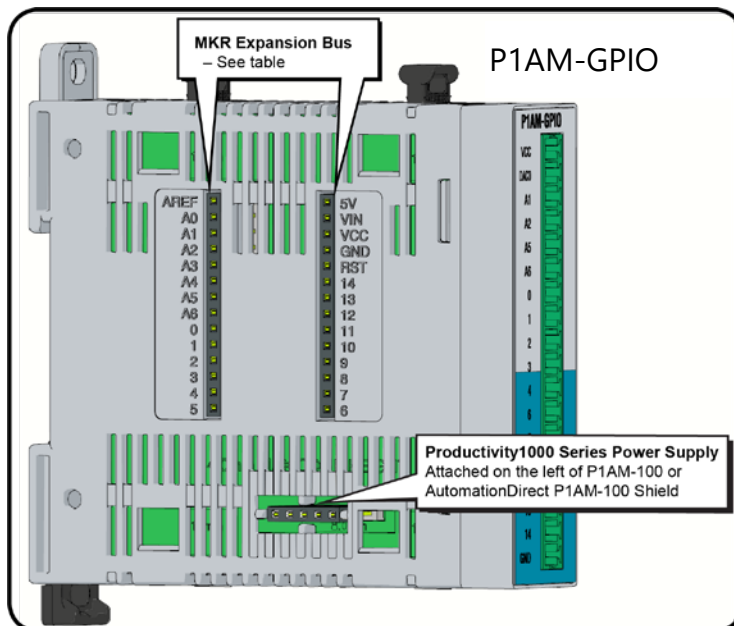
The P1AM-GPIO is a housed Arduino MKR form factor shield that brings a subset of the MKR header pins to the front faceplate with an 18-position terminal block. These pins include

basic overvoltage, undervoltage, and overcurrent protection. It connects to the left side of the P1AM-100 CPU and most Arduino MKR form factor shields.



| General Specifications | |
|------------------------------|---|
| Operating Temperature | 0° to 60°C (32° to 140°F) |
| Storage Temperature | -20° to 70°C (-4° to 158°F) |
| Humidity | 5 to 95% (non-condensing) |
| Environmental Air | No corrosive gases permitted |
| Vibration | IEC60068-2-6 (Test Fc) |
| Shock | IEC60068-2-27 (Test Ea) |
| Heat Dissipation | 475mW |
| Enclosure Type | Open Equipment |
| Module Location | Connects to the left side of the <u>P1AM-100</u> CPU. |
| Weight | 56g (2.0 oz.) |
| Agency Approvals | UL 61010-1 and UL 61010-2-201 File E139594, Canada & USA CE |

Terminal block connector sold separately.
Recommended connector options
P2-RTB or P2-RTB-1.



| MKR Expansion Bus Pins | |
|---------------------------|------------------------|
| GPIO | A0–A6, 0–14 |
| Analog Input Pins | A0–A6 |
| Analog Output Pins | A0 |
| PWM Pins | 0–8, 10, A3, A4 |
| Interrupt Pins | 0, 1, 4–8, A1, A2 |
| 5V | 5V supply output |
| Vin | 5V regulated supply |
| VCC | 3.3 V supply output |
| GND | Ground |
| RST | Reset |
| AREF | Analog Input Reference |

Critical Notes:

Pins A3, A4, and 8–10 are used for the base controller.
Do not exceed 46mA combined from pins 0, 1, and 4–10.
Do not exceed 3.3 V on any I/O pin.
Do not exceed 7mA on any I/O pin.
Do not apply power to 5V or VCC

!WARNING!

Do not add or remove modules
with field power applied!